

ULTRA LOW-PROFILE (ULP) PCIe GEN 7 CONNECTORS CABLE ASSEMBLIES

TE Connectivity (TE's) Ultra Low-Profile (ULP) PCIe Gen 7 products deliver cutting-edge PCIe Gen 7 electrical performance and with an ultra low-profile mating height to support next generation datacenter and artificial intelligence (AI) applications. This innovative design not only delivers cutting-edge speed data rates of 128 gigatransfers per second (GT/s) but also can fit into tight spaces, such as beside a CEM connector slot, under heatsinks, or near chips. The centrally located sidebands ensure superior signal integrity and improved host board routability, enhancing the manufacturability and overall performance of your system.

APPLICATIONS

- · Data Center
- AI / Machine Learning (ML)
- Server
- Storage

KEY BENEFITS

· Faster Data Rates with Smaller Form Factor

This product can help achieve faster data rates without compromising on the size of the form factor with superior SI performance, making it ideal for next-generation applications that require both speed and compactness.

· Enhanced Usability in Dense Applications

The low-profile mating height allows the solution to be used in locations that were previously impossible due to height constraints, expanding the range of potential applications.

· Superior Host Board Routability

The centrally located sidebands improve the manufacturability of the host board and enhance the signal integrity performance of the end-to-end channel, helping to ensure reliable and high-quality data transmission.

• Full Portfolio Solution

Customers can leverage this product family for a variety of applications, from X4/X8/X16 PCle applications to different configurations, providing a comprehensive solution for their needs.

KEY FEATURES

• PCIe Gen 7 Electrical Performance

Our products are fully capable of delivering the high-speed data transmission promised by PCle Gen 7, with a transmission speed of 128 GT/s PAM4 using the PCle protocol.

· Ultra Low-Profile

With a mating height less than 9mm, this product can fit into tight spaces that were previously inaccessible, such as beside a CEM connector slot, under heatsinks, or near chips.

Central Sideband

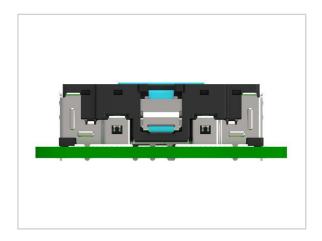
The centrally located sidebands accommodate PCle transmission, ensuring optimal signal integrity and performance.

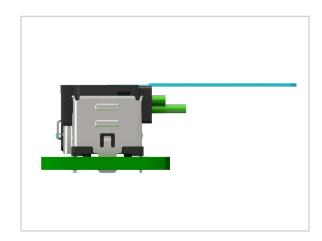
Platform Product

This is a versatile platform product that covers the primary use cases of PCIe, including X4/X8/X16 configurations and various applications such as Right Angle, Straight, and Lateral.

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