

INTRODUCING SLIVER CONNECTORS FOR SFF-TA-1002

- Highest performing card edge connector for SFF-TA-1002
- Supports high speed and density for next generation silicon



Our new industry-leading Sliver connectors have been adopted as the standard into SFF-TA-1002 and are the highest performing card edge connectors in the standard. The SFF-TA-1002 pinout supports EDSFF, PCIe and OCP NIC standards, enabling easy design-in by consolidating fragmented pinouts and speeds.

KEY BENEFITS

- Highest performing card edge connector for SFF-TA-1002
- Pinout supports EDSFF, PCIe and OCP NIC standards
- Enables easy design-in and multi-sourcing by consolidating fragmented pinouts and speeds
- Supports high speeds through PCIe Gen 5, with a roadmap to 112G
- Supports next gen silicon PCIe lane counts where current products in the market begin to max out
- Power connectors enable high power to riser cards and hardware accelerators
- Reduced cables increase airflow throughout the enclosure
- Competitively priced compared to cables and other power card edge products

LEARN MORE

[Sliver Landing Page](#)

[Sliver Product Brochure](#)

[Sliver Sandbox Video](#)

[Sliver Parts List](#)

APPLICATIONS

- EDSFF drives
- Riser cards
- Custom high-speed card-edge applications
- Advanced differential memory

ELECTRICAL

- Voltage rating: 30VDC
- Current rating: 55A Max (power connectors only)

MECHANICAL

- Operating temperature: -55 to 105°C

MATERIALS

- Housing: LCP UL 94-V0
- Contact: Gold plated copper alloy

STANDARDS & SPECIFICATIONS

- SFF-TA-1002
- EDSFF
- OCP NIC
- PCIe