

REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
	A1	REVISED PER ECR-19-000980	24JAN2019	SR	JDB

△ Material:  
 COVER : LCP, black, UL-94-V0  
 HEADER : Black WH-9100, UL-94-V0  
 LEAD FRAME: C5191-HV170-190 °  
 SOLDER PIN PLATING: 7 μm (275 μinch) TIN 1.27 μm (50 μinch) NICKEL OVER ALL

- 2 THE PART IS RECOMMENDED FOR REFLOW SOLDERING PROCESS PEAK  
 SOLDERING: TEMPERATURE MAX. +260 °C/-5 °C, MAX. 30s
- 3 OPERATING TEMPERATURE: T= -40 °C TO +105 °C
- 4 STORAGE TEMPERATURE: T= -40 °C TO +85 °C
- 5 DC CURRENT/VOLTAGE RATING-PSE PINS: 720mA @57V(CONTINUOUS)
- 6 MEET IEEE 802.3at REQUIRMENT
- △ UNLESS OTHERWISE SPECIFIED, SEE TABLE FOR ALL DIMENSIONS TOLERANCES
- 8 PACKAGING: TUBE ACCORDING TO PACKAGING SPECIFICATION 107-18128

SUGGESTED PCB LAYOUT

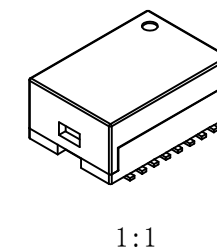
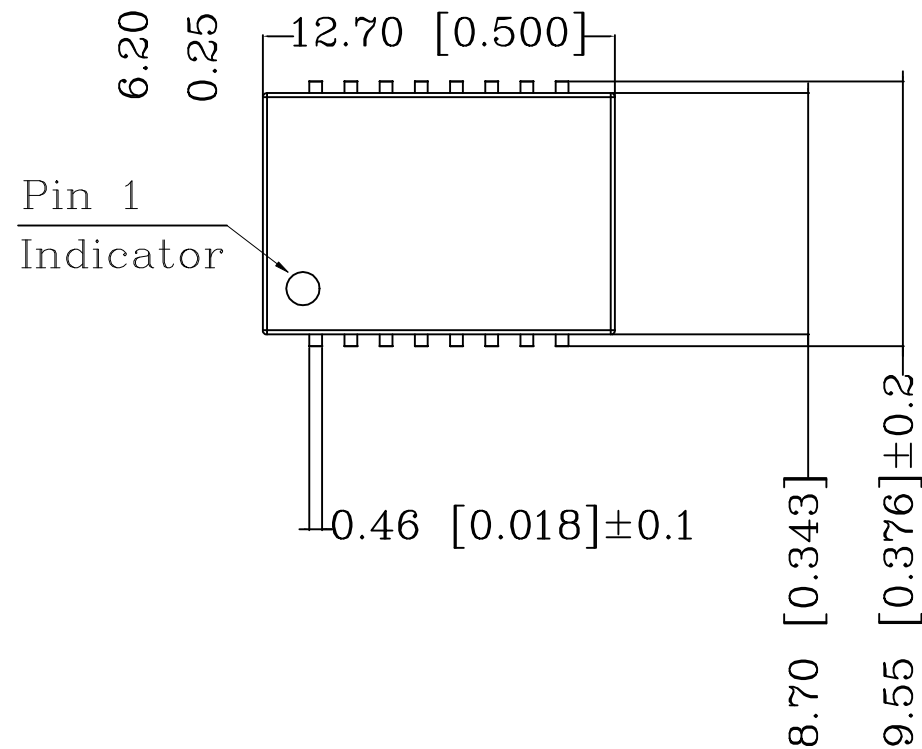
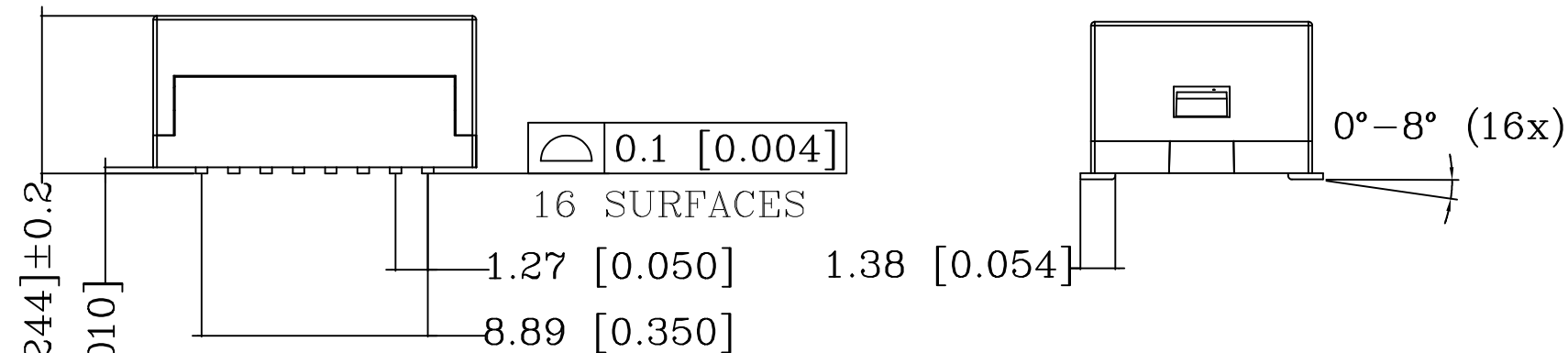
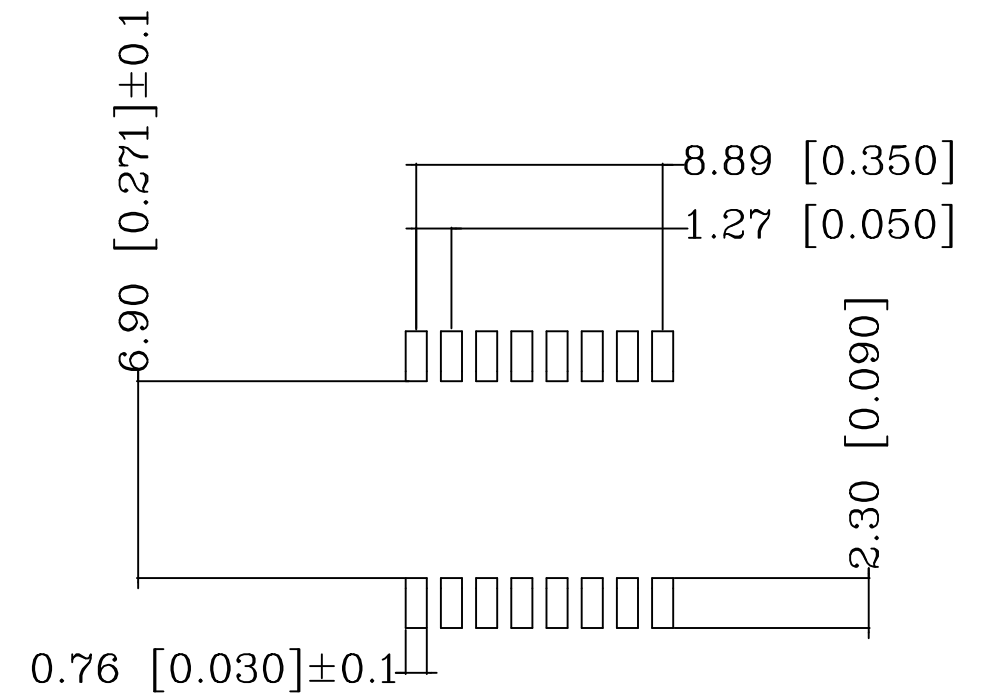


TABLE OF TOLERANCE (mm)	
RANGE	TOLERANCE
0-10	±0.15
>10-40	±0.25
>40-70	±0.30
>70	±0.40

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN	18JAN2019		<b>TE</b> TE Connectivity												
DIMENSIONS: mm [INCHES]		CHK	18JAN2019														
TOLERANCES UNLESS OTHERWISE SPECIFIED:		A.C. NGOUMGANG															
<table border="0"> <tr><td>0 PLC</td><td>± -</td><td rowspan="5" style="text-align: center;">7</td></tr> <tr><td>1 PLC</td><td>± -</td></tr> <tr><td>2 PLC</td><td>± -</td></tr> <tr><td>3 PLC</td><td>± -</td></tr> <tr><td>4 PLC</td><td>± -</td></tr> <tr><td>ANGLES</td><td>± -</td></tr> </table>		0 PLC	± -	7		1 PLC	± -	2 PLC	± -	3 PLC	± -	4 PLC	± -	ANGLES	± -	APVD	18JAN2019
0 PLC	± -	7															
1 PLC	± -																
2 PLC	± -																
3 PLC	± -																
4 PLC	± -																
ANGLES	± -																
MATERIAL		J. DE BRUIJN			NAME												
FINISH		PRODUCT SPEC			DISCR.ETH.MAG. POE 10/100 SINGLE SMT												
1		108-94693			SIZE												
1		APPLICATION SPEC			CAGE CODE												
		114-94555			DRAWING NO												
		WEIGHT			RESTRICTED TO												
		CUSTOMER DRAWING			A3												
		SCALE			5:1												
		SHEET			1 of 2												
		REV			A1												

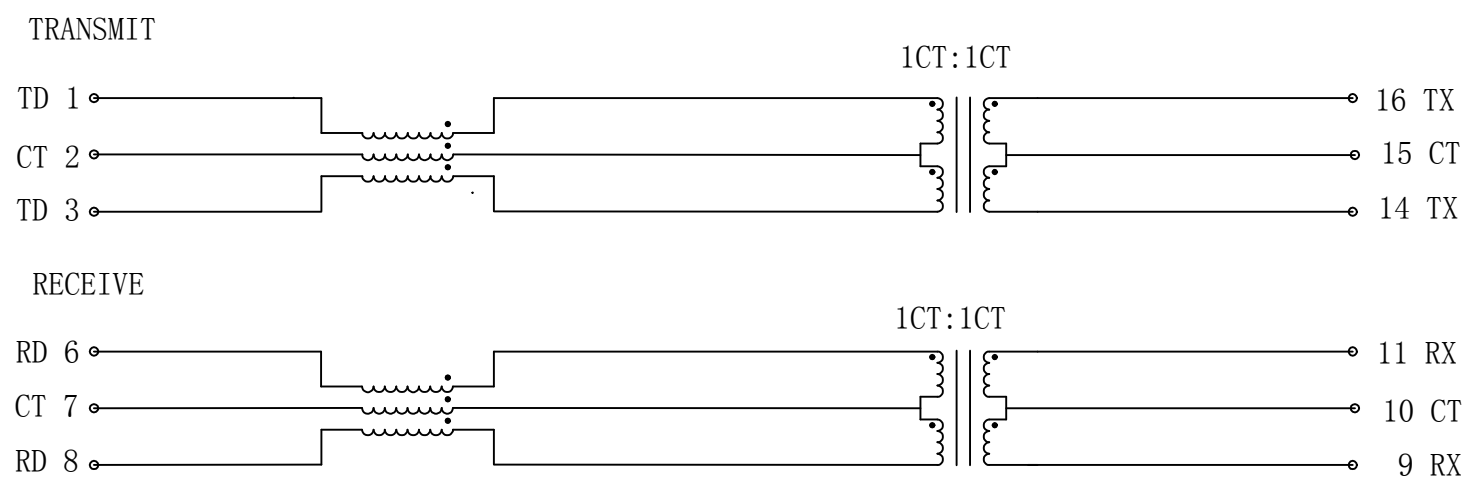
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P	LTR	DESCRIPTION	DATE	DWN	APVD
-	-	SEE SHEET 1	-	-	-

CIRCUIT DIAGRAM



Electrical Specifications @ 25 °C (Unless other specified meet IEEE 802.3 Specification)		
Turn Ratio	1CT:1CT ±2%	
Inductance (OCL) @100kHz/0.1V with 8mA DC Bias	350 µH min.	
Insertion Loss	0.1-100MHz	-1 dB max.
	1-30MHz	-18 dB min.
Return Loss (Zout = 100 OHMS)	40MHz	-14.4 dB min.
	50MHz	-13.1 dB min.
	80MHz	-12 dB min.
Crosstalk	30MHz	-40 dB min.
	60MHz	-35 dB min.
	100MHz	-30 dB min.
DCMR	30MHz	-40 dB min.
	60MHz	-35 dB min.
	100MHz	-30 dB min.
Isolation Voltage (Input - Output)	1500 Vrms	



CONTROL CODE

DATE CODE

"0213"

YY-YEAR

WW-WEEK

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DIMENSIONS: mm [INCHES]		CHK A.C. NGOUMGANG 18JAN2019		
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD J. DE BRUIJN 18JAN2019	PRODUCT SPEC	
0 PLC ± - 1 PLC ± - 2 PLC ± - 3 PLC ± - 4 PLC ± - ANGLES ± -		APPLICATION SPEC 108-94693 114-94555	SIZE A3	
MATERIAL		WEIGHT	CAGE CODE	DRAWING NO
FINISH		CUSTOMER DRAWING	00779	5-2337822-4
		SCALE		RESTRICTED TO
		3:1		-
		SHEET		REV
		2 of 2		A1