



- ① HOUSING: PCT POLYESTER, BLACK, UL 94V-0.
IR REFLOW PROCESS COMPATIBLE.
TERMINALS: 0.305 THICK PHOS BRONZE PLATED
WITH 1.27 μ m MINIMUM THICK HARD GOLD IN
LOCALIZED AREA AND 2.03 μ m MINIMUM THICK
MATTE TIN IN SOLDER AREA OVER 1.27 μ m
MINIMUM THICK NICKEL UNDERPLATE.
SHIELD: 0.196 THICK COPPER ALLOY PLATED
WITH 1.27 μ m MINIMUM NICKEL AND
2.03 μ m MINIMUM HOT TIN DIP ON PCB
GROUND TABS.
- ② JACK CAVITY CONFORMS TO FCC RULES AND
REGULATIONS, PART 68 SUBPART F
- ③ CENTER BOARD LOCATOR AND PCB HOLE
REQUIRED FOR ALL, EXCEPT -1
- ④ PCB HOLES/TABS USED FOR -4
- ⑤ PCB HOLES/TABS USED FOR -2, -3, AND -5
- ⑥ USE #30 DRILL BIT OR 3.25mm DRILL
BIT WHEN PRODUCING THESE PCB HOLES
- ⑦ PCB HOLES/TABS USED FOR -6
8. RECOMMENDED APPLICATIONS: FAST ETHERNET,
10 BASE-T ETHERNET, AND 10/100 BASE-T
ETHERNET (CAT 5 TWO PAIR 1/2, 3/6)