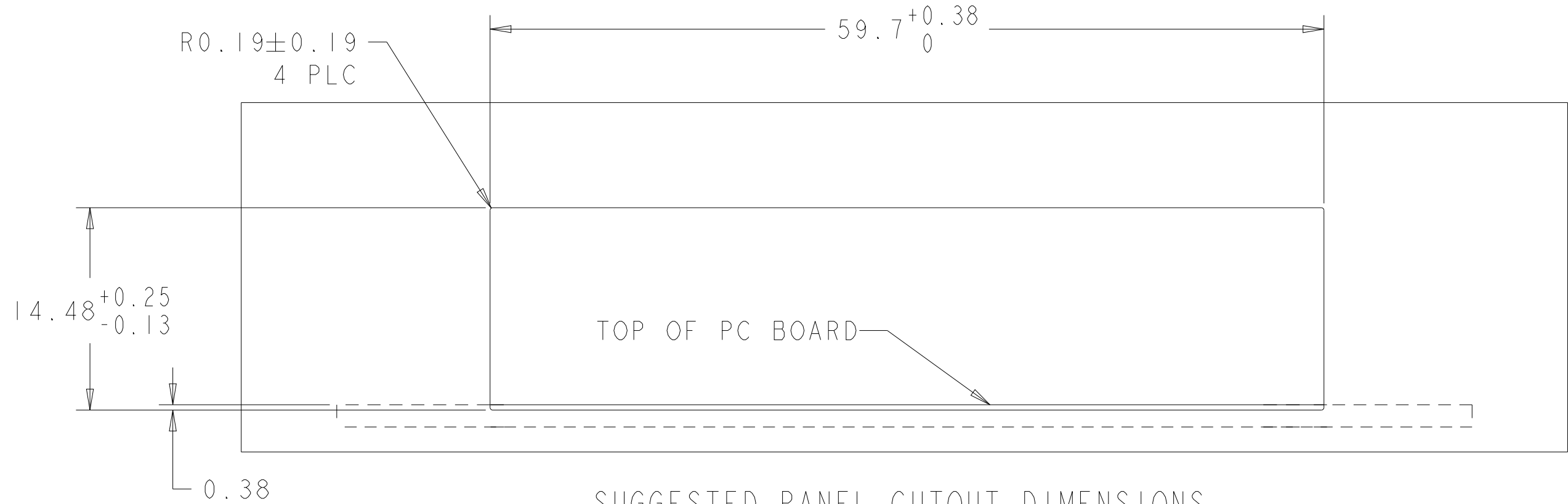
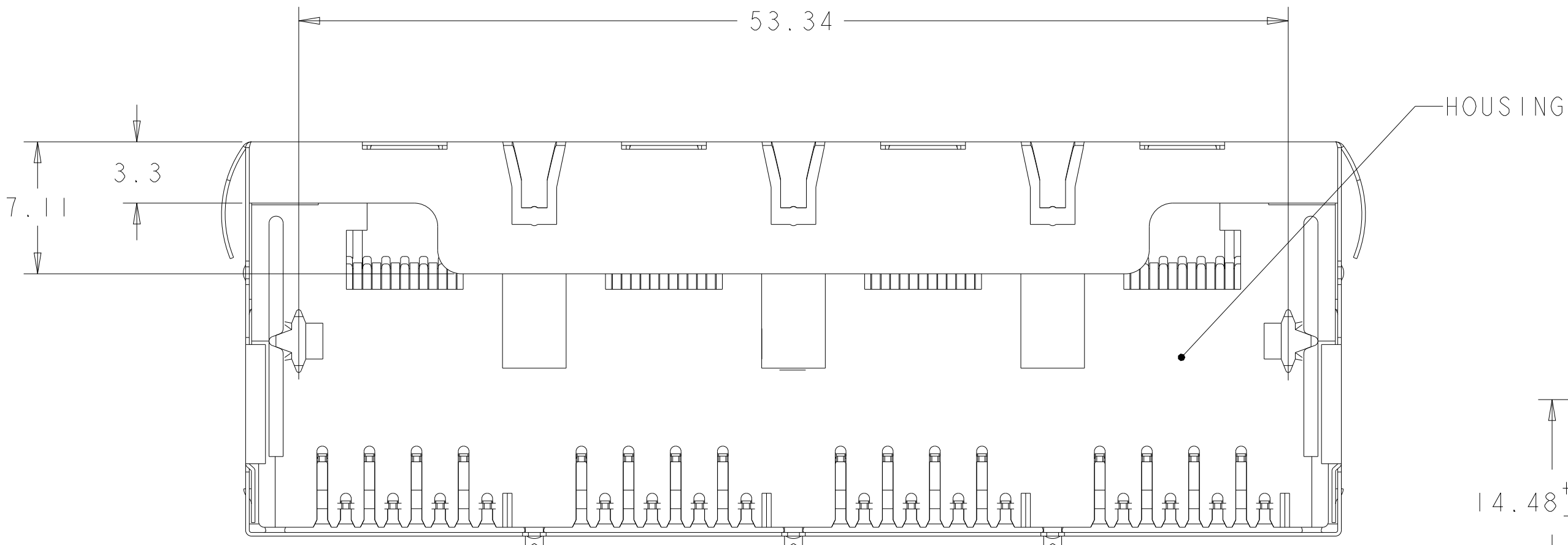
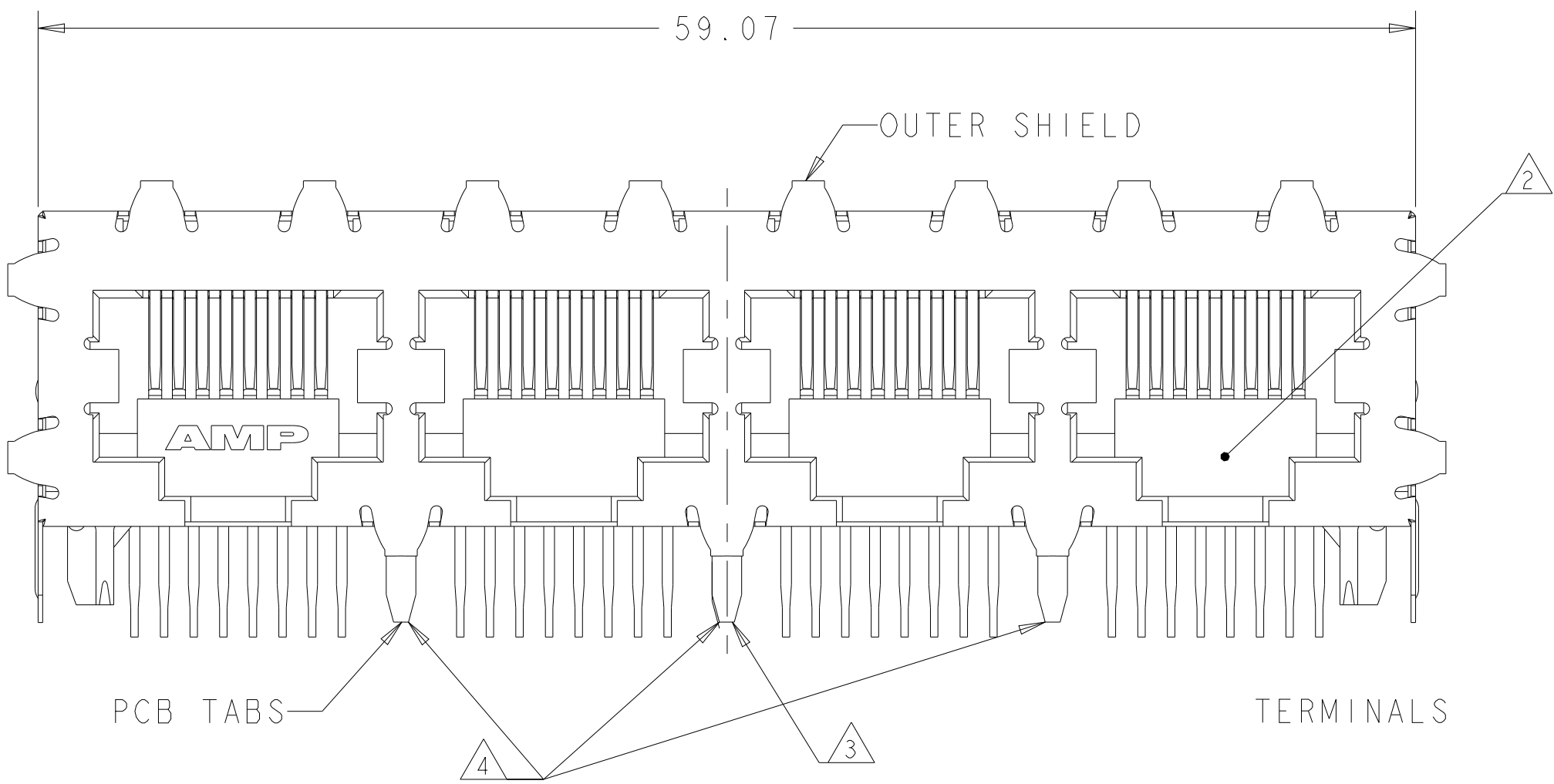
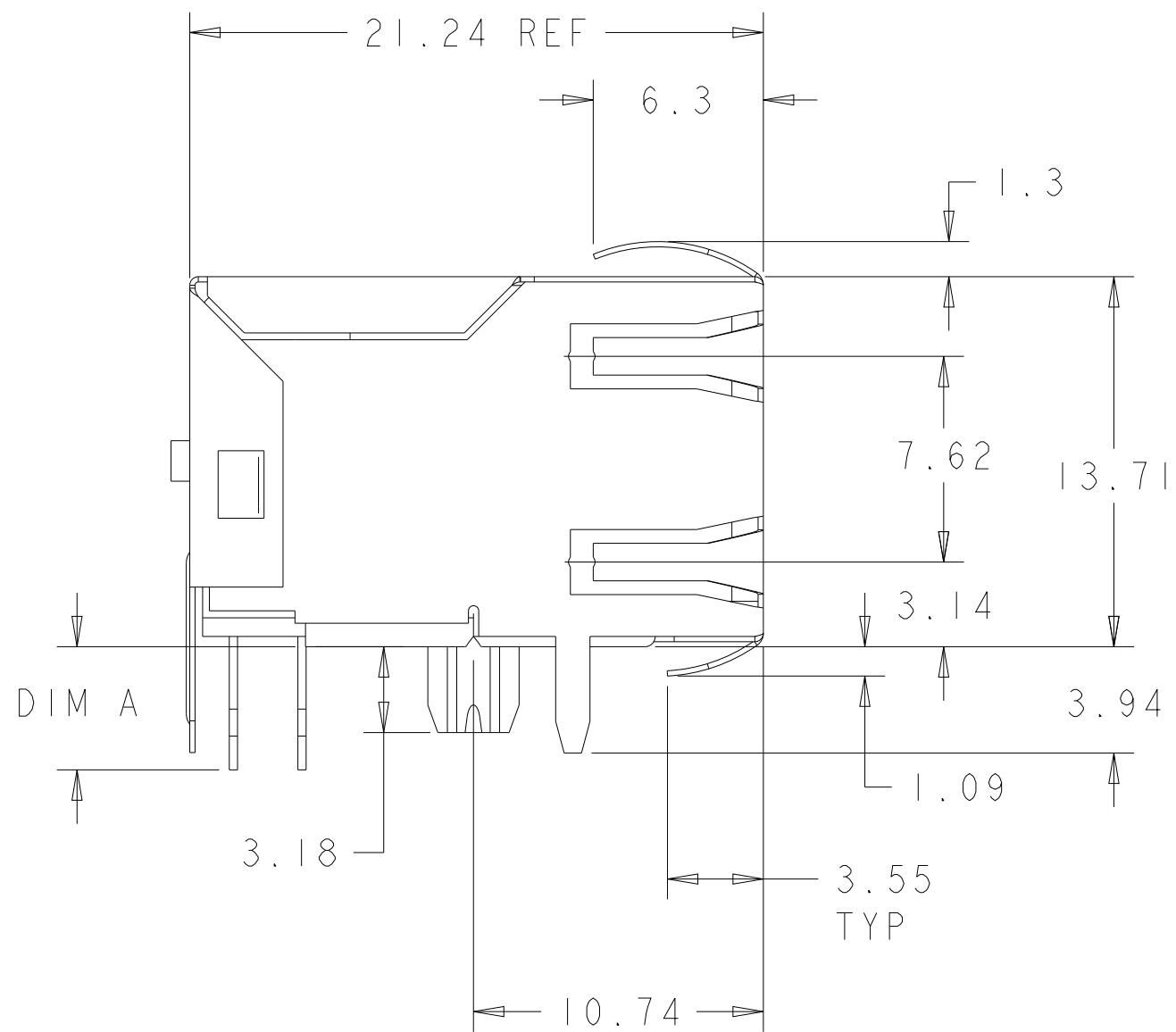
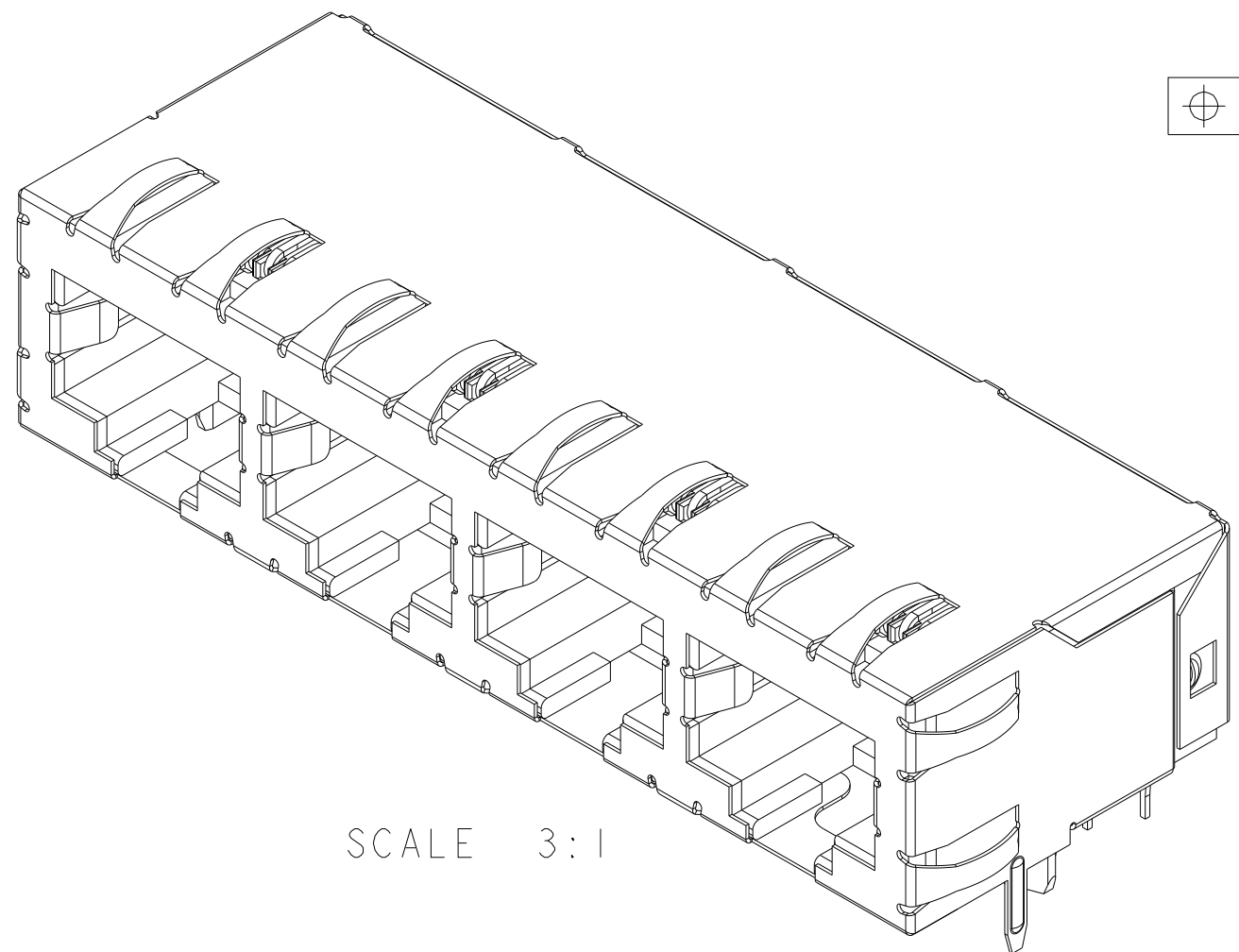


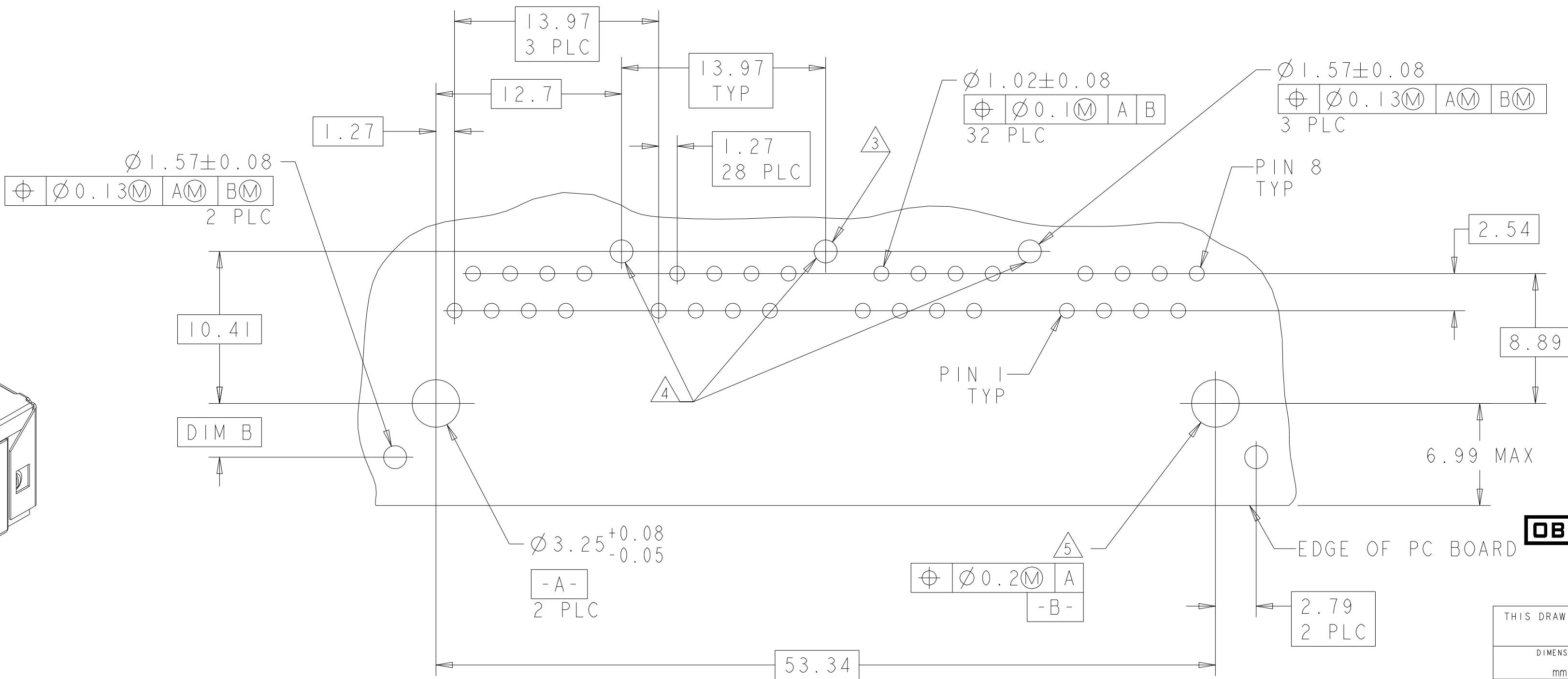
REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	A4	REVISED PER ECN-25-320055	01MAY2025	TR	RK
	A5	REVISED PER ECN-25-330528	03JUL2025	TR	RK



SUGGESTED PANEL CUTOUT DIMENSIONS
SCALE 3:1



SCALE 3:1



PC BOARD VIEWED FROM COMPONENT SIDE OF BOARD
SCALE 4:1

1. MATERIAL: HOUSING - PCT POLYESTER, BLACK, UL 94V-0
TERMINALS: 0.305 THICK PHOS BRONZE PLATED WITH .0013mm MINIMUM THICK HARD GOLD IN LOCALIZED AREA AND .002mm MINIMUM THICK MATTE TIN IN SOLDER AREA OVER .0013mm MINIMUM THICK NICKEL UNDERPLATE
SHIELD - .196 THICK COPPER ALLOY PLATED WITH .0013mm MINIMUM NICKEL AND .002mm MINIMUM HOT TIN DIP ON PCB GROUND TABS
- 2 JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS, PART 68 SUBPART F
- 3 PCB HOLES/TABS USED FOR -3
- 4 PCB HOLES/TABS USED FOR -2 AND -4
- 5 USE #30 DRILL BIT OR 3.25mm DRILL BIT WHEN PRODUCING THESE PCB HOLES
6. RECOMMENDED APPLICATIONS: FAST ETHERNET, 10 BASE-T ETHERNET, AND 10/100 BASE-T ETHERNET (CAT 5 TWO PAIR 1/2, 3/6)

3.68	3	3.56	6339167-4
4.57	1	3.56	6339167-3
4.57	3	3.56	6339167-2
4.57	0	3.05	6339167-1
DIM B	NUMBER OF REAR PCB TABS	DIM A	PART NO.

OBSELETE

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN S. SCHOLL/J. A. MATYR 09MAY2005	TE Connectivity	
DIMENSIONS:		CHK J. WESTMAN 09MAY2005		
mm	TOLERANCES UNLESS OTHERWISE SPECIFIED:	APVD S. FLICKINGER 09MAY2005	NAME ASSEMBLY, MULTI-PORT, 1X4, 8 POSITION, SHIELDED, WAVE PANEL GROUND	
	0 PLC ±	PRODUCT SPEC	SIZE 108-1163-9	RESTRICTED TO
	1 PLC ±.25	APPLICATION SPEC		
MATERIAL SEE NOTE 1	2 PLC ±	114-2048	A100779C=6339167	REV A5
	3 PLC ±	WEIGHT -		
CUSTOMER DRAWING		SCALE 4:1	SHEET 1 OF 1	