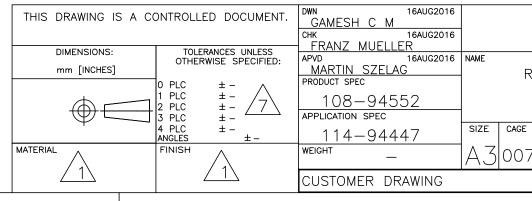
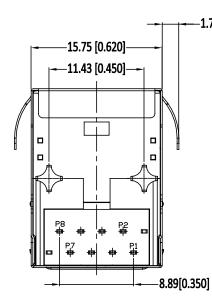
4		3	2			
C 2016 TE Connectivity. All Rights Reserved.	I					REVISI
				P LTR		DESCRIF
				A2	RETURN LOSS	SPECIFICATION
				LI		
	1.75±0.25 (6x)					
15.75 [0.620]	-					
<u> </u> →11.43 [0.450]			CONNECTOR MATERIAL:			
	- A		HOUSING: LCP BLACK UL94 V-0			
			INSERT: LCP BLACK UL94 V-0			
			SHIELD: BRASS			
			SHIELD PLATING	i: NICKEL		
			CONTACT: COPPER ALLOY			
	r -		CONTACT PLATING: SELECTIVE GOLD, MIN. 0.76			
			OVER MIN. 1.27µm (50µinch			
P8 P2			SOLDER PIN PLA			
					/ER ALL	,
	L.		SHIELDING PIN			

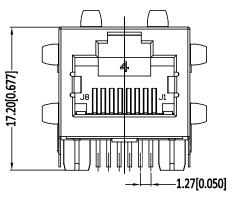
- 2 PIN NOT ELECTRICALLY CONNECTED MAY **DIAGRAM FOR OMITTED PINS**
- **3** RJ45 CAVITIY DIMENSION CONFORM TO F
- **4 THE PART IS RECOMMENDED FOR REFLOW** SOLDERING: TEMPERATURE MAX. +260°C
- 5 OPERATING TEMPERATURE: T= -40°C TO +
- 6 STORAGE TEMPERATURE: T= -40°C TO +8
- 🗥 UNLESS OTHERWISE SPECIFIED, SEE TABLE
- 8 JACK CONFIGURATION: 1 x 1
- TAB DIRECTION: UP
- AUTOMATED PLACEMENT: FOR FORCE-FR FUNCTION WIDEN TO MIN. 3.40 mm DIAN 10 PACKAGING: TRAY ACCORDING TO PACK

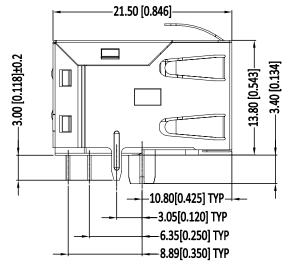


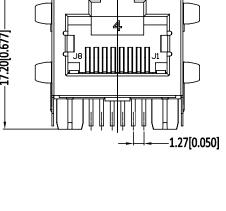
1:1

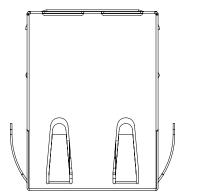












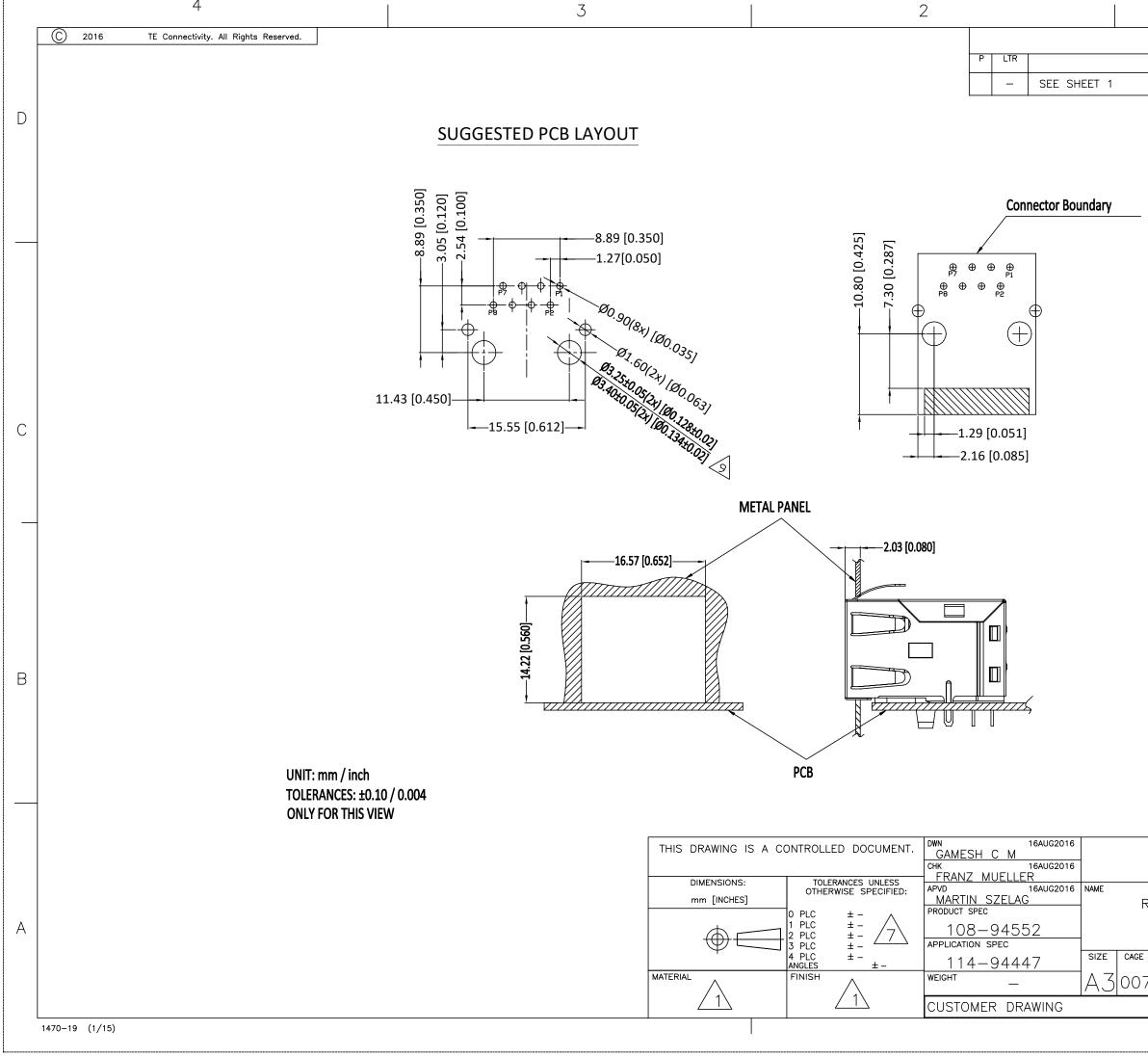
D

С

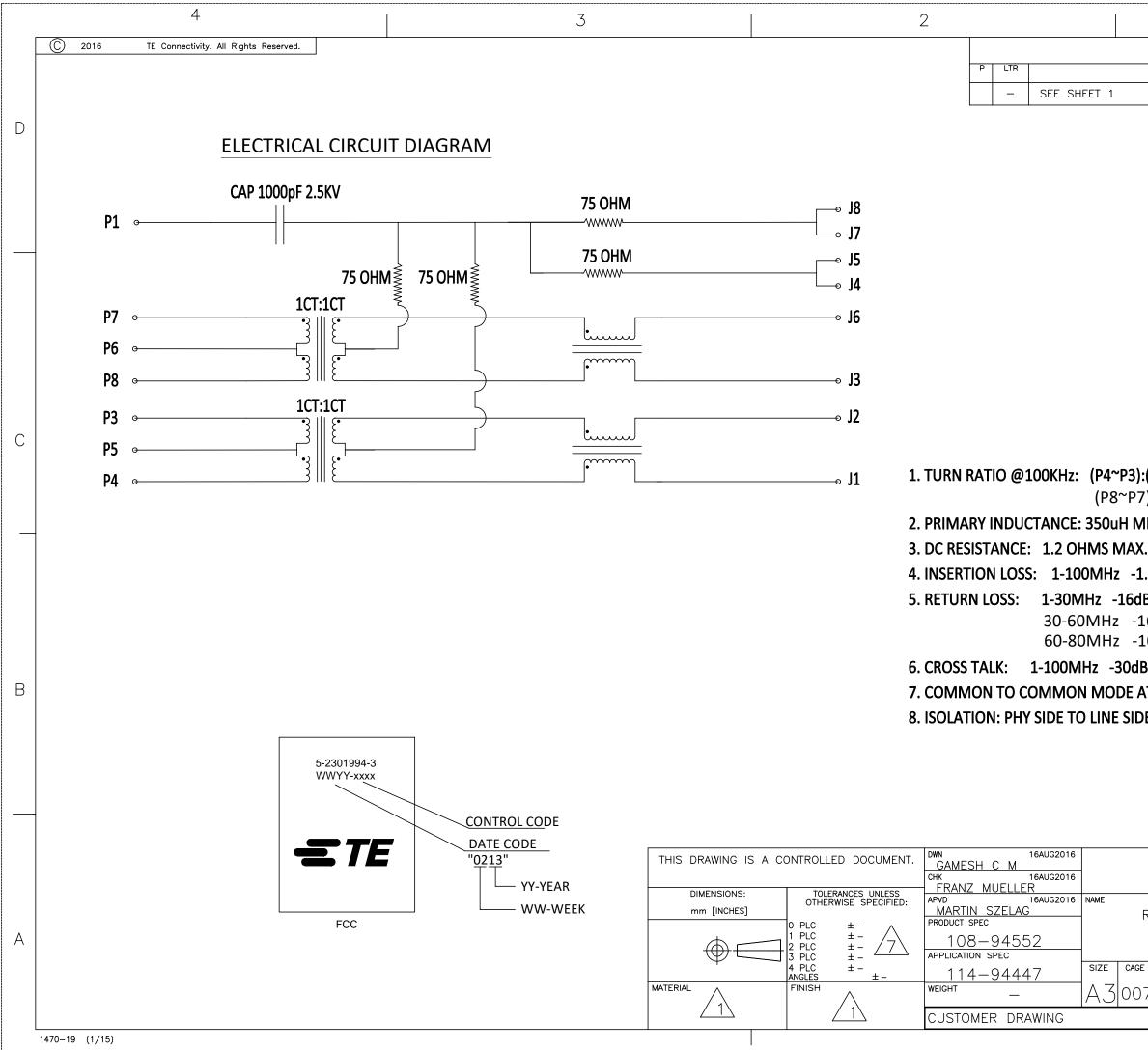
В

А

REVISIONS				
DESCRIPTION	DATE	DWN	APVD	
IFICATION CORRECTED	02JUN2021	SS	OW	
				D
			-	
0.76μm (30μinch) IN	CONTACT ARI	ĒA		
µinch) NICKEL		_, (
IN OVER 1.27μm (50	µinch) NICKEL			
E OMITTED SEE ELEC		г		
		•		
CC PART 68 / SOLDERING PROCES	S PEAK			С
MAX. 10s	-			0
35°C 5°C				
FOR ALL DIMENSION	S TOLERANCES	,		
E ASSEMBLY WITHOU	JT THE ALIGN	MENT	-	
GING SPECIFICATION	107-18116			
				В
				D
	ABLE OF TO		NCE	
	(mm)			
	RANGE TO	DLERA ±0.1		
	>10-40	±0.2		
	>40-70	±0.3		
	>70	±0.4	0	
STE TE (Connectivity Lt	d.		
J45 JACK INT.MAG. 1	0/100 1x1 IN	١V		
				А
CODE DRAWING NO		RESTRI	CTED TO	
code drawing no 79 C=5-230199	94-3	RESTRI	CTED TO	



1				
REVISIONS				
DESCRIPTION	DATE	DWN	APVD	
	_	—		
				D
			_	
				С
			-	
				_
				В
				-
TE Conne	ectivity Lt	d		
RJ45 JACK INT.MAG. 10/10	00 1×1 IN	IV		
				А
CODE DRAWING NO		RESTRIC	TED TO	
779 C =5-2301994-3	3	_	_	
	ET 2 OF 3	ς REV	A2	
		~		



	1				
REVISIONS					
DESCRIPTION		DATE	DWN	APVD	
		-		_	
					D
					0
					C
:(J1~J2) = 1:1±2%					
'):(J3~J6) = 1:1±2%					
IIN. @100KHZ,0.1V 8r	nA DC	BIAS			
, .•					
.2dB MAX.					
B MIN.					
.6dB+20log(f/30MH L0dB MIN.	lz) MIN	۷.			
B MIN.	ALI				В
TTENUATION: 1-100N	VIHZ -:	SUAB IVIIN.			D
E: 2250VDC					
	0	1 • . • 1	-1		
	Conne	ectivity Lt	a.		
RJ45 JACK INT.MAG.	10/10	00 1×1 IN	V		
	,				А
CODE DRAWING NO			RESTRIC	CTED TO	
779 C -5-23019	94	3	_	_	
SCALE 3	CUE		REV	′A2	
	• 1			<u> </u>	