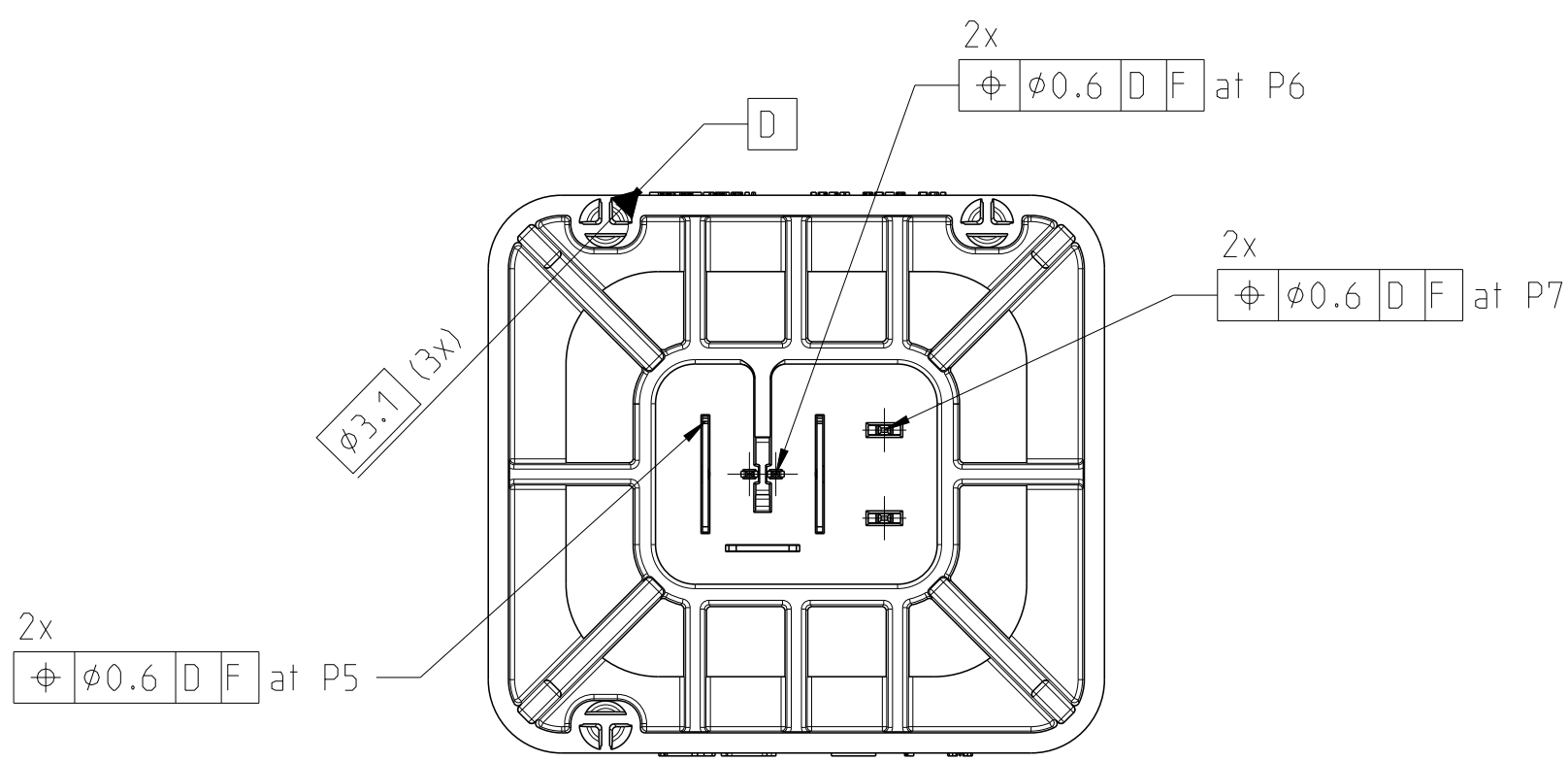
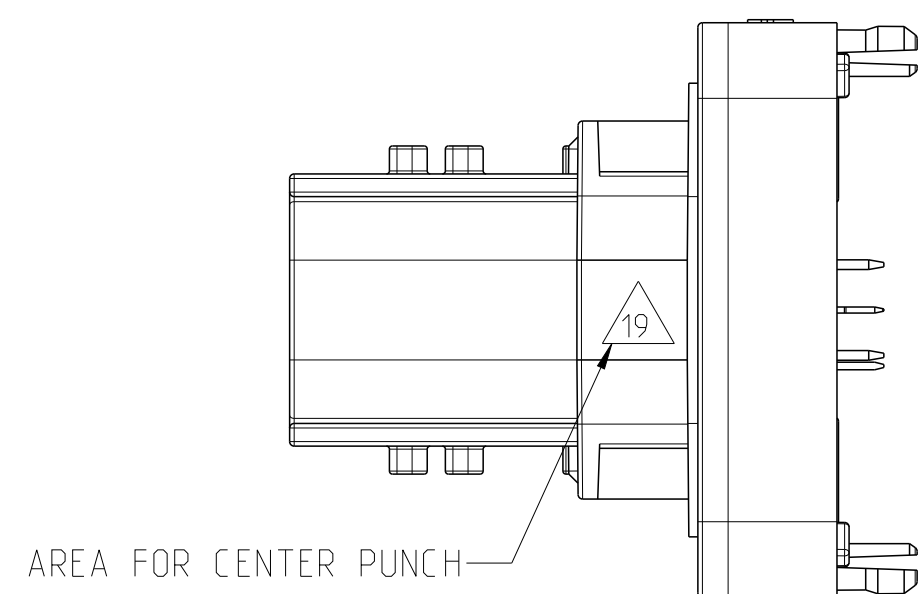
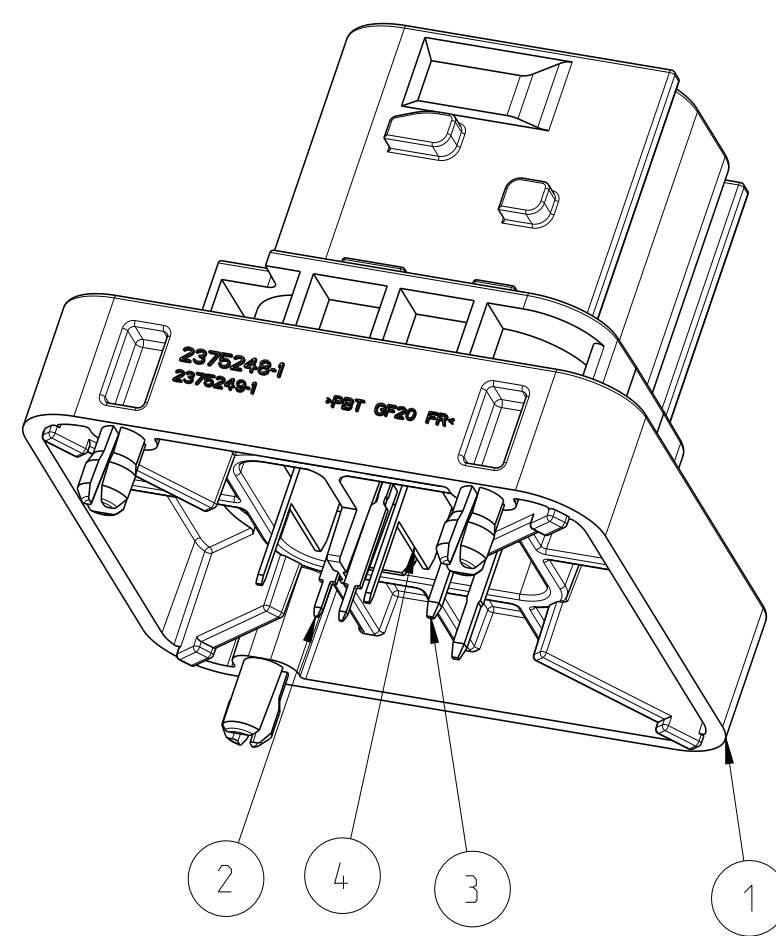
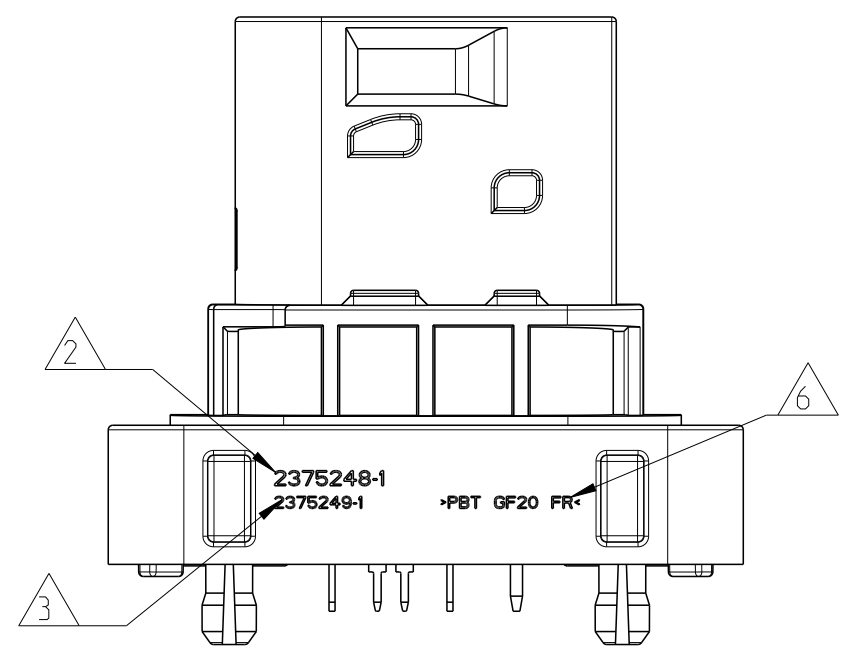
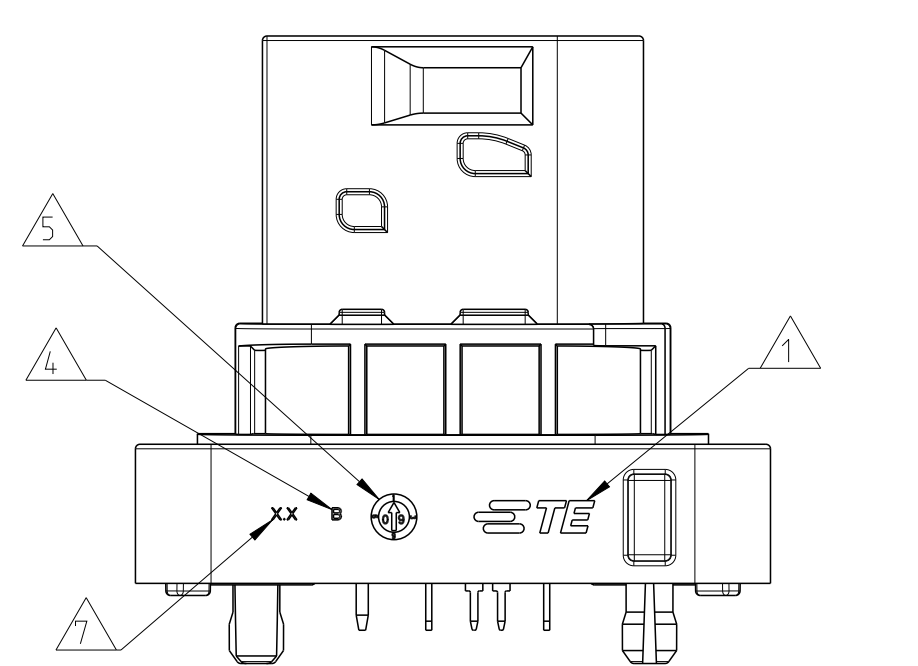
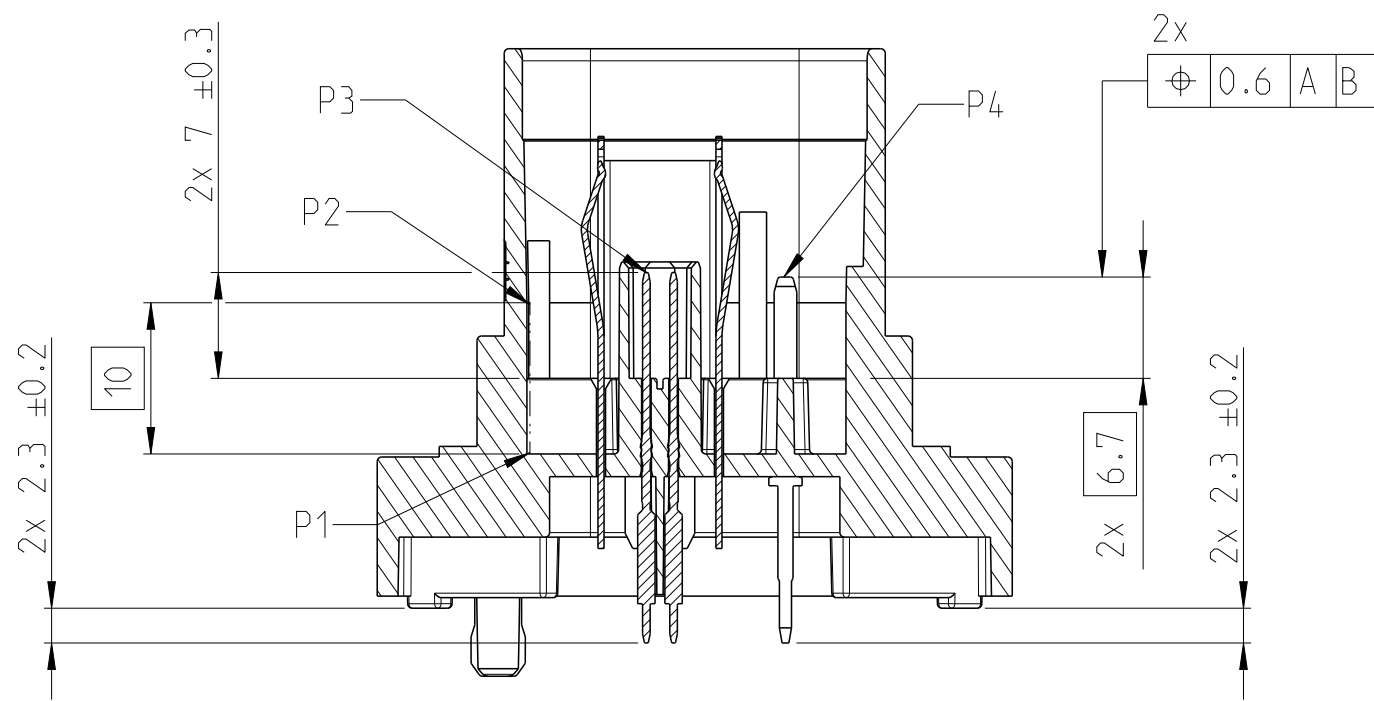
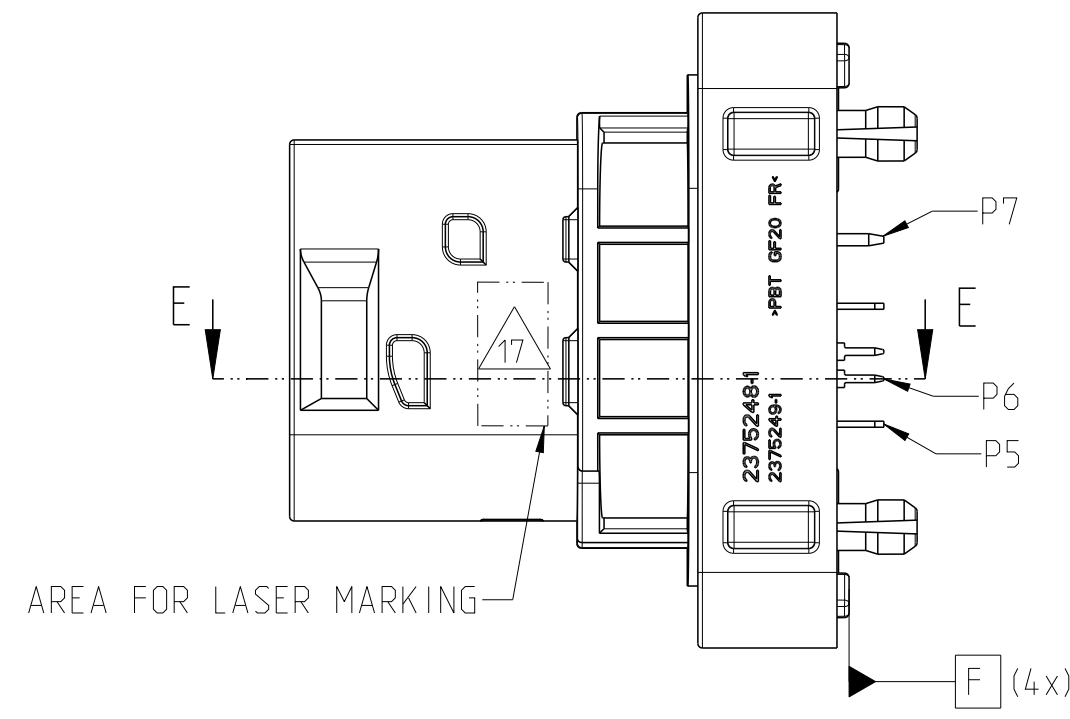
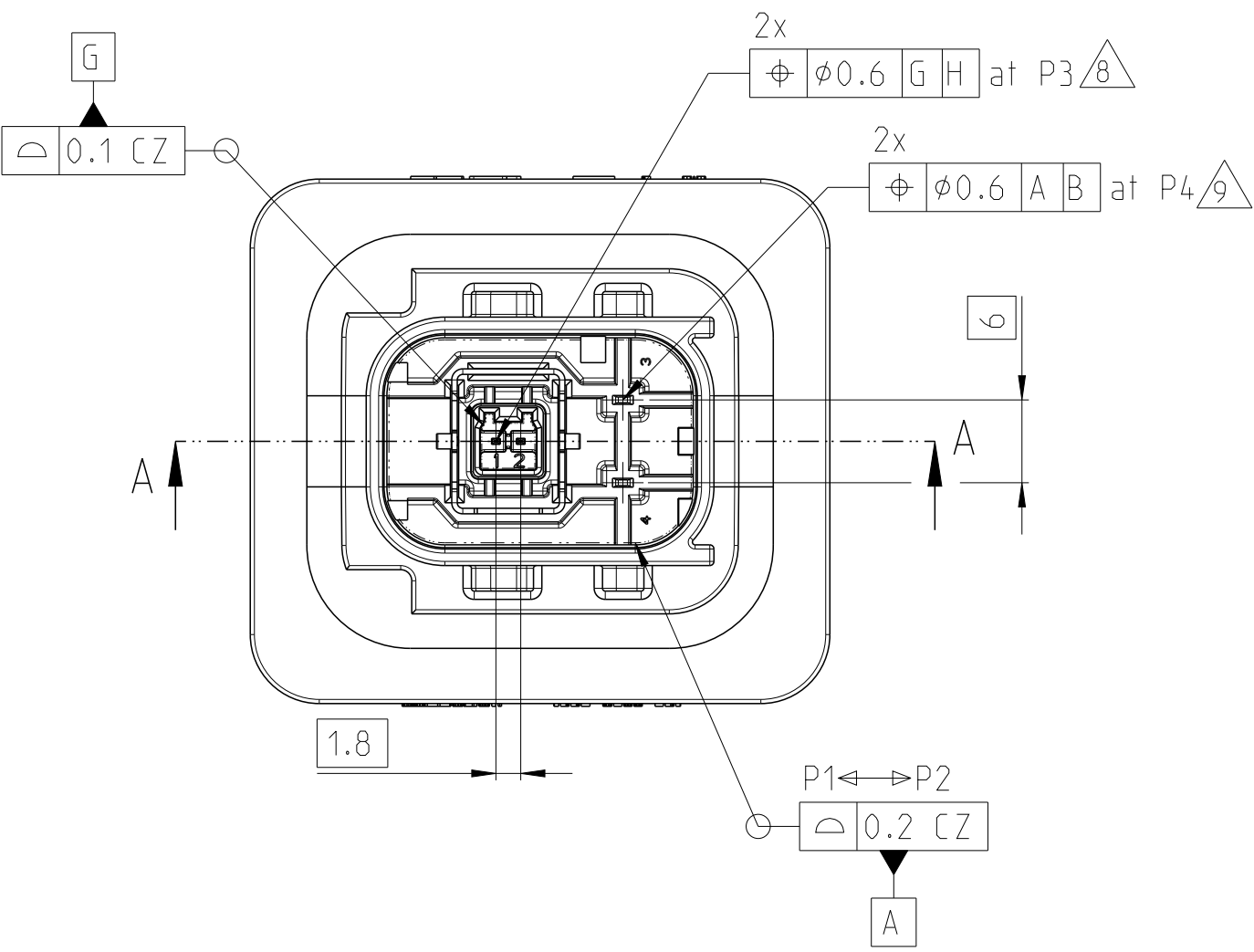
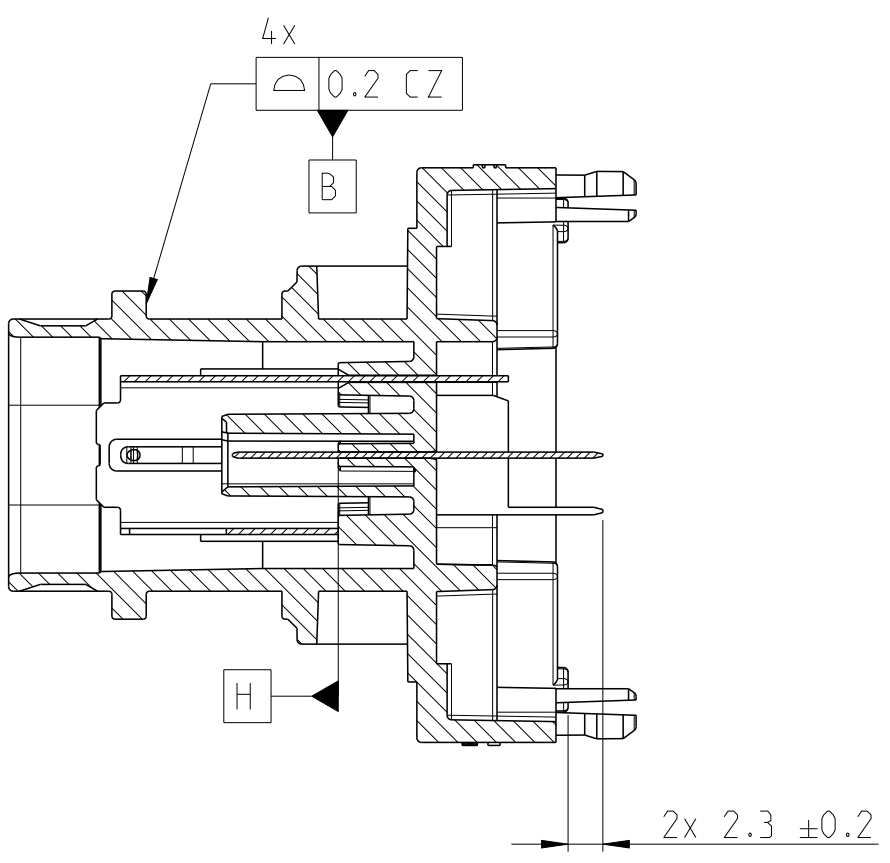




SECTION A-A



SECTION E-E



NOTES:

Bemerkungen:

- TE CONNECTIVITY (TE) LOGO
TE Connectivity (TE) Logo
- TE ASSEMBLY-NUMBER
TE Baugruppennummer
- TE SINGLE PART NUMBER
TE Einzelteilnummer
- LETTER INSERT FOR THE TOOL REVISION.
Schriftzeile für Werkzeugänderungsindex.
- PRODUCTION DATE WITH DATE-CLOCK
Produktionsdatum mit Datumsuhr
- MATERIAL MARKING ACCORDING TO VDA 260
Materialkennzeichnung nach VDA 260
- MOLD CAVITY MARKING
Nestmarkierung
- PLATING SPECIFICATION 0.5 TAB:
CONTACT AREA: MIN. 4MM FROM CONTACT TIP >3µm ELECTROPLATED SILVER OVER NICKEL
SOLDER TAIL AREA: MIN. 3.5MM FROM SOLDER TAIL TIP COVERED WITH 3-8µm TIN OVER NICKEL UNDERPLATING: ELECTROPLATED NICKEL
Beschichtungsspezifikation 0.5 Tab:
Kontaktbereich: min. 4mm von der Kontaktspitze >3µm galvanisch Silber über Nickel
Lötlbereich: min. 3.5mm von der Lötspitze 3-8µm verzinkt über Nickel Grundbeschichtung: galvanisch Nickel
- PLATING SPECIFICATION 1.5 TAB:
CONTACT AREA: MIN. 6MM FROM CONTACT TIP >3µm ELECTROPLATED SILVER OVER NICKEL
SOLDER TAIL AREA: MIN. 4MM FROM SOLDER TAIL TIP COVERED WITH 3-10µm TIN OVER NICKEL UNDERPLATING: ELECTROPLATED NICKEL
Beschichtungsspezifikation 1.5 Tab:
Kontaktbereich: min. 6mm von der Kontaktspitze >3µm galvanisch Silber über Nickel
Lötlbereich: min. 4mm von der Lötspitze 3-10µm verzinkt über Nickel Grundbeschichtung: galvanisch Nickel
- TIP OF SOLDER TAIL CAN BE INSERTED INTO RECOMMENDED PCB (t=1.2 OR t=1.6)
THE DESIGN IS OPTIMIZED FOR A PCB WITH t=1.6±0.16mm ONLY.
Die Lötspitze kann in die empfehlenden PCBs eingeführt werden (t=1.2 oder t=1.6)
Das Design ist auf ein PCB mit t=1.6±0.16mm optimiert.
- PACKED IN TRAY ACCORDING PACKAGING SPEC. V2375248
Verpackt im Tray entsprechend Verpackungs-Spezifikation V2375248
- TO BE MATED WITH HDSCnet CONNECTOR 9-2354261-1
Zu verwenden mit HDSCnet Stecker 9-2354261-1
- 100% ELECTRIC. CONTINUITY (LV TEST),
SHORT CIRCUIT (HV TEST) AND PRESENCE TESTING
100% elektrische-, Durchgangs-(LV Test), Kurzschluss-(HV Test) und Anwesenheitsprüfung
- MIN. RETENTION FORCE:
min. Ausdrueckkraft:
0.5 TAB: 15N 25MM/MIN
1.5 TAB: 20N 25MM/MIN
- THE HEADER WILL BE SOLDERD BY LEAD FREE WAVE SOLDER PROCESS
Die Messerleiste wird mit einem bleifreien Wellen-Lötprozess verlötet.
- USE GLOVES FOR MANUAL HANDLING
Handschuhe bei manueller Handhabung verwenden
- PRODUCTION DATE OF ASSEMBLY: DDMMYYYY 2 DIGIT DAY 3 LETTER MONTH 4 DIGIT
YEAR : E.G. 17NOV2016
Produktionsdatum der Baugruppe: TTMMJJJJ 2 Zahlen Tag 3 Buchstaben Monat 4 Zahlen Jahr Bsp. 17NOV2016
- DIFFERENT CODINGS
Unterschiedliche Kodierung
- CENTER PUNCH : IDENTIFY B-SAMPLE ASSEMBLY EXAMPLE 9-xxxxxx-x
- THE USAGE OF NON-VALIDATED B-SAMPLES FOR THE PURPOSE OTHER THAN FIT-FORM IS NOT INTENDED BY TE AND SHALL BE AT CUSTOMER OWN RISK

FOR PROTOTYPE B-SAMPLE ONLY
NUR FUER B-MUSTER

REVISIONS				
P	LTR	DESCRIPTION	DATE	DWN APVD
A		NEW DRAWING	09FEB2021	HPS JB
A1		Drawing update	29MAR2024	MS WR

ITEM NO.	REV	MATERIAL	DESCRIPTION
4	A	CrNi	HD MATEnet Shield
3	B	CuSn	1.5x0.64mm TAB
2	A	CuSn	0.5x0.4mm TAB
1	B	PBT GF20 / V-0	HD MATEnet 1+2 180° Header HSG. C

9-2375248-6	F	A	1	2	2	1	RED rot	6
9-2375248-5	E	A	1	2	2	1	WHITE weiss	5
9-2375248-4	D	A	1	2	2	1	BLUE blau	4
9-2375248-3	C	A	1	2	2	1	GREEN gruen	3
9-2375248-2	B	B	1	2	2	1	GREY grau	2
9-2375248-1	A	B	1	2	2	1	BLACK schwarz	1

TE ASSY NO.	CODING	REV	Qty. reqd per Assy	COLOUR	ITEM NO.

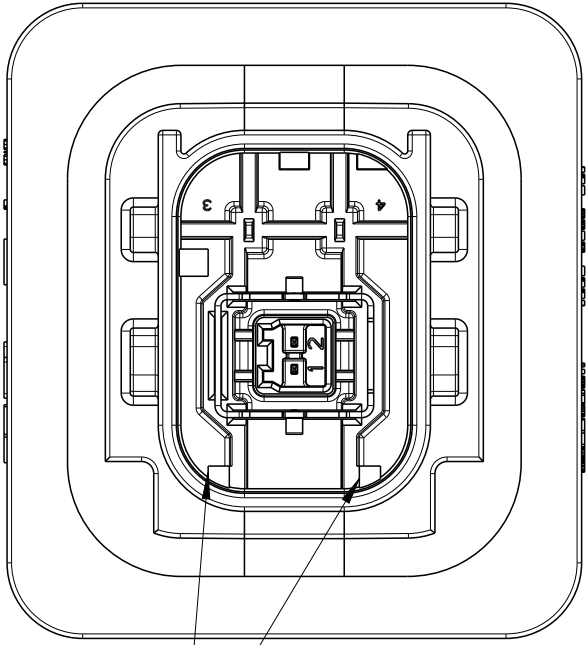
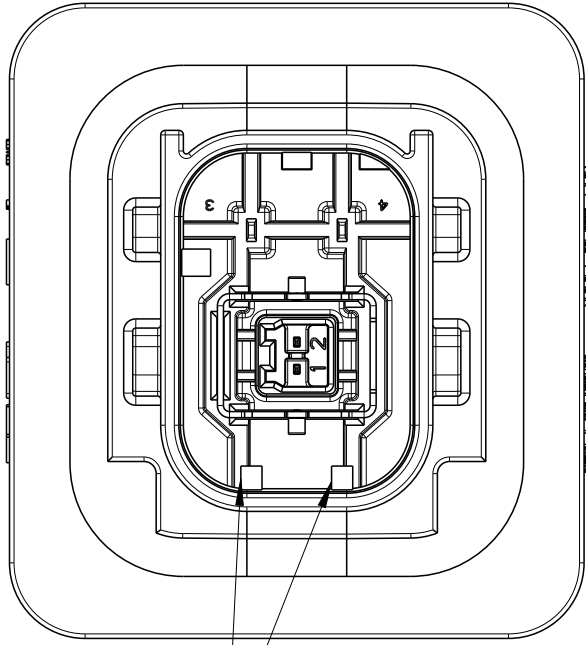
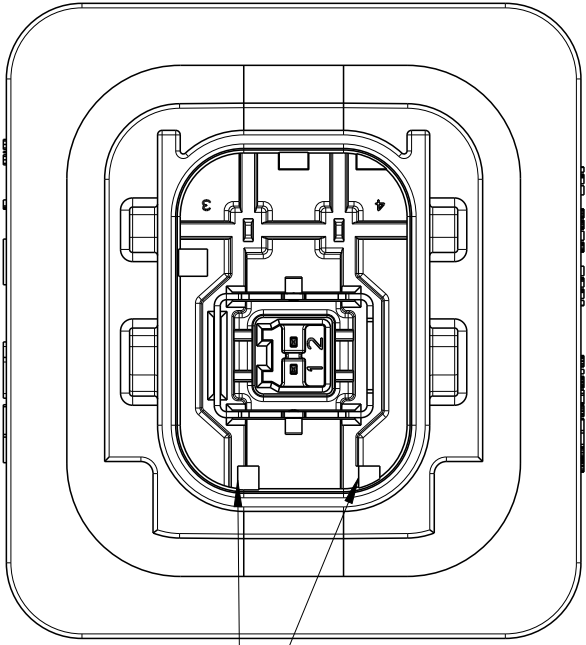
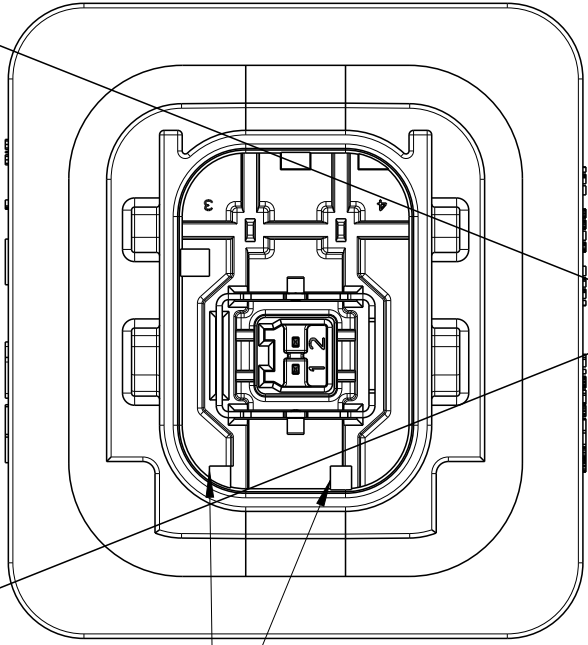
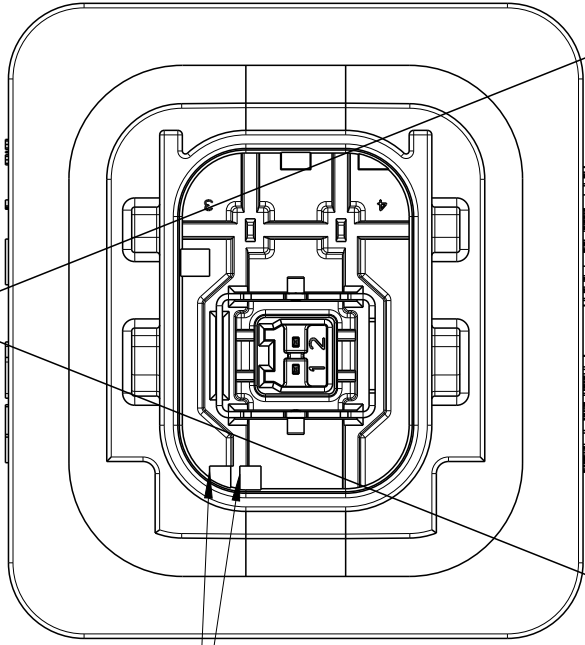
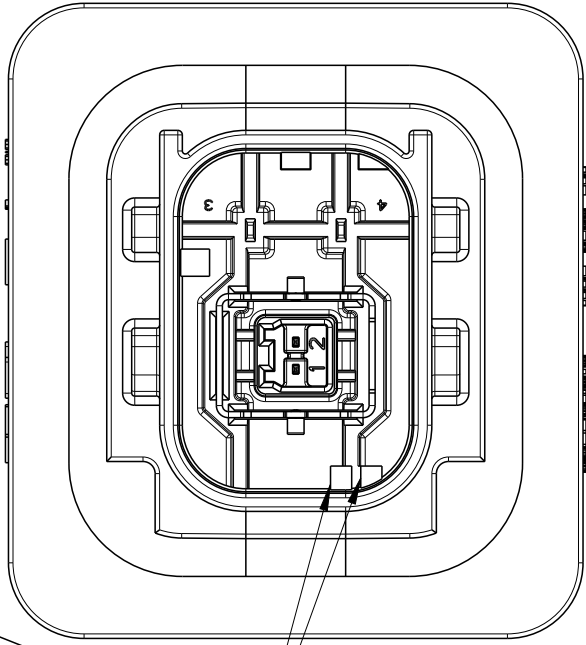
THIS DRAWING IS A CONTROLLED DOCUMENT.	DWN 07MAY2021 CHK SUKSHITHA H P APVD VISHNU PATAGAR	NAME HD MATEnet 1+2pos HDR ASSY GRP C, 180DEG HD MATEnet 1+2 pol. HDR Geh. Grp. C, 180 Grad
DIMENSIONS: mm	TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±0.1 1 PLC ±0.05 2 PLC ±0.01 3 PLC ±0.001 4 PLC ±0.0001	PRODUCT SPEC APPLICATION SPEC
MATERIAL	FINISH	SIZE CAGE CODE DRAWING NO 00779 C=2375248
		RESTRICTED TO

CUSTOMER DRAWING	SCALE 2:1	SHEET 1 OF 3	REV A1
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DIMENSION AND TOLERANCES ACCORDING TO:
Bemerkungen und Toleranzen gemäß:
DIN ISO 20457
DIN EN ISO 8015 (A1) - DIN EN ISO 291
DIN EN ISO 14405-1/-2/-3

9-2375248-X

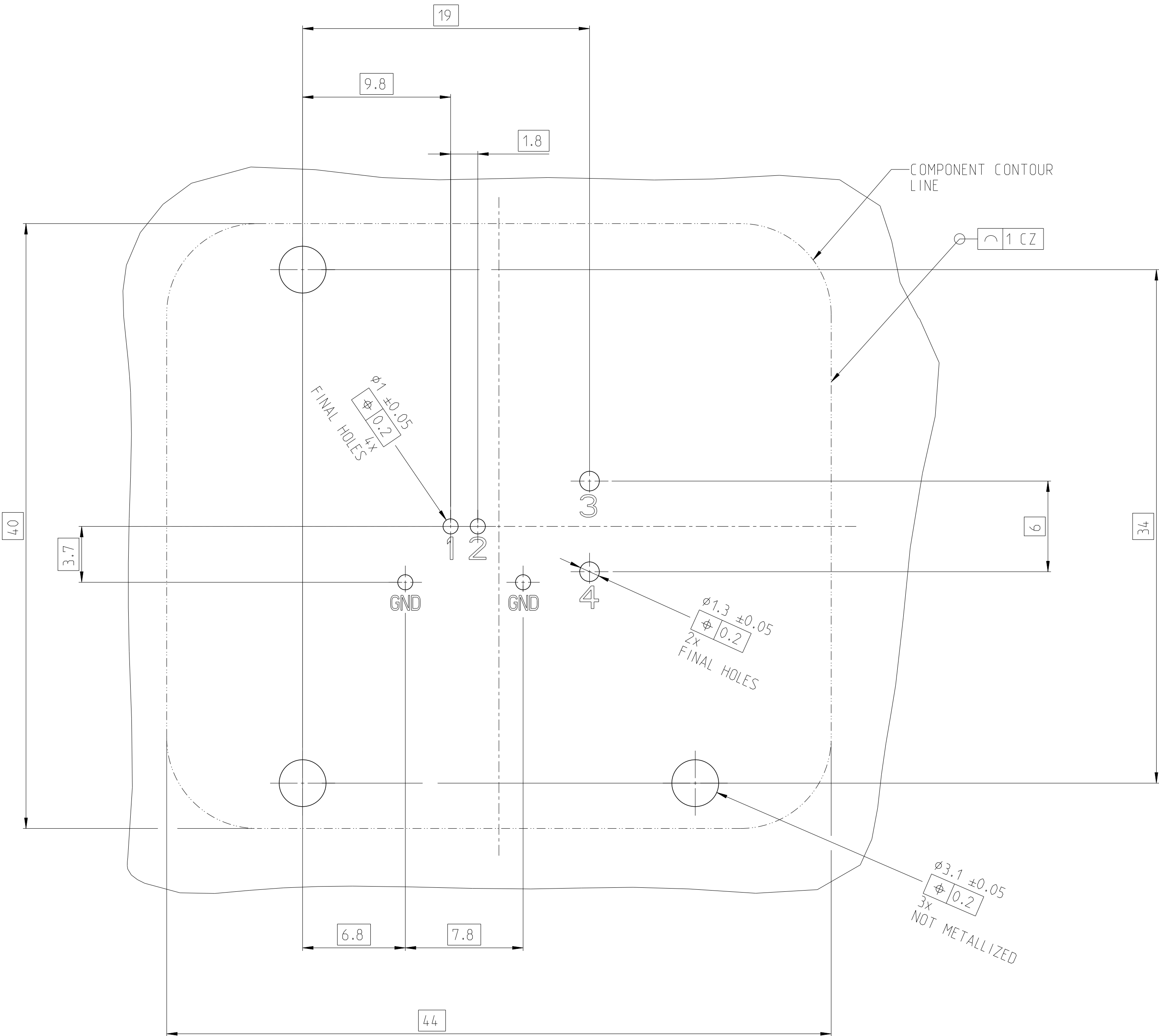
HD MATEnet 1+2 Header 180° Assy C- Regular

CODING "A" Kodierung	CODING "B" Kodierung	CODING "C" Kodierung	CODING "D" Kodierung	CODING "E" Kodierung	CODING "F" Kodierung
 PN: 9-2375248-1	 PN:9-2375248-2	 PN: 9-2375248-3	 PN: 9-2375248-4	 PN: 9-2375248-5	 PN: 9-2375248-6

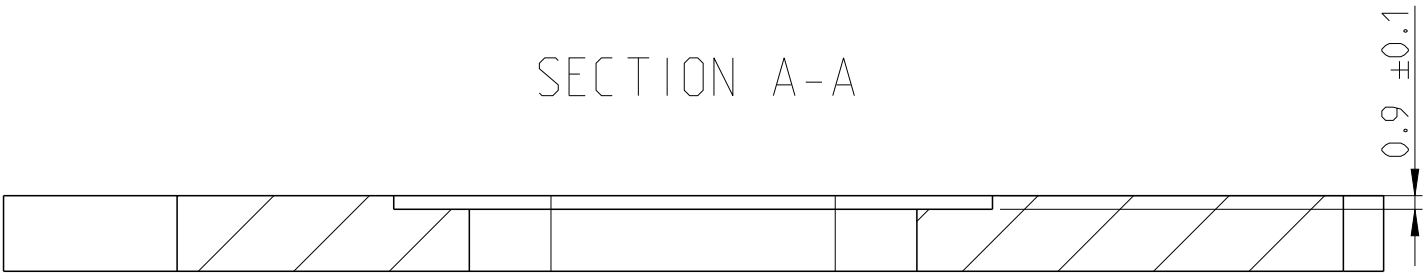
FOR PROTOTYPE B-SAMPLE ONLY
NUR FUER B-MUSTER

REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
-	-	SEE SHEET 1	-	-	-

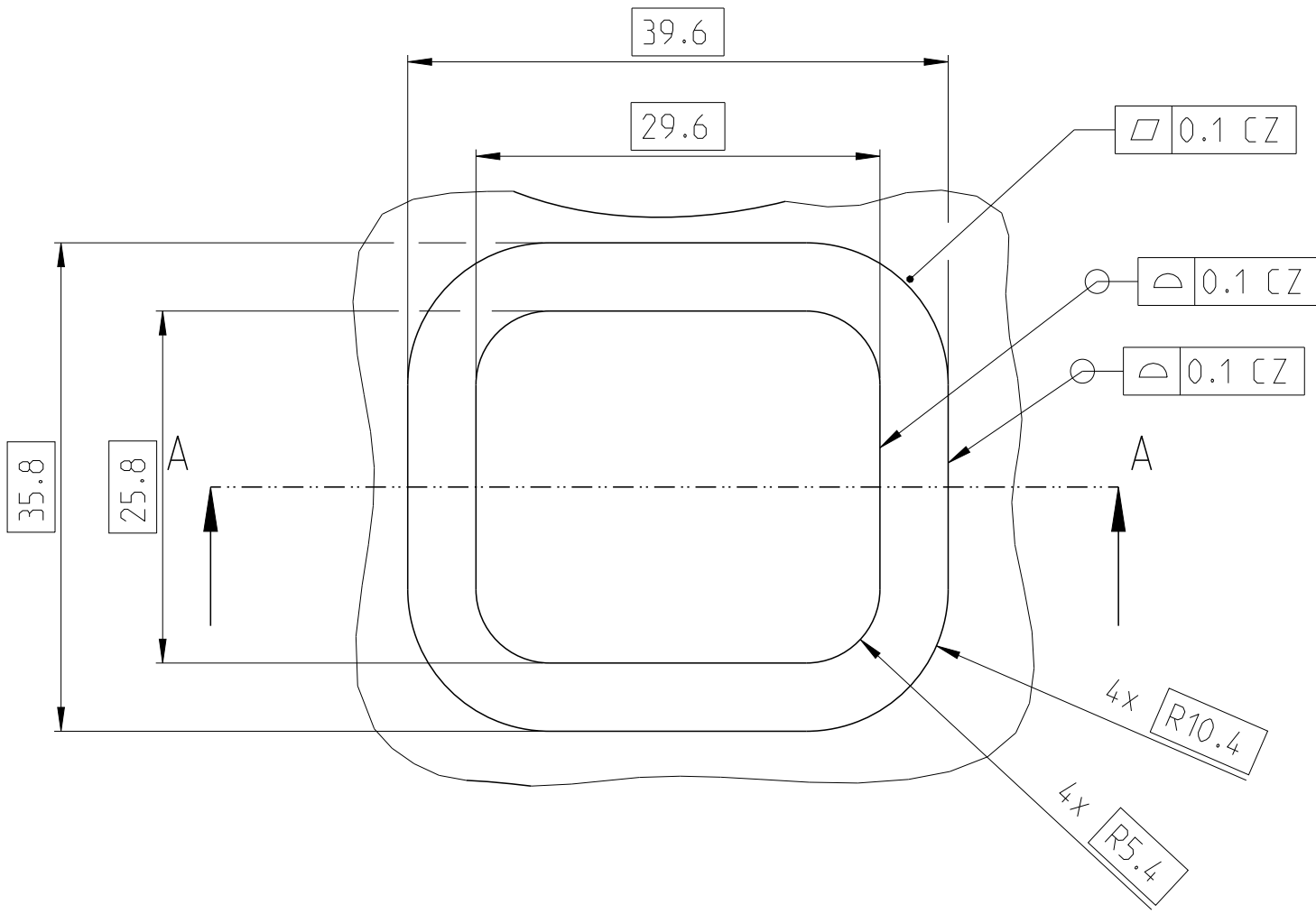
PCB LAYOUT
Platinen Layout)
RECOMMENDED PCB LAYOUT / CUSTOMER IS RESPONSIBLE FOR LAYOUT
Empfohlenes Platinen Layout / Kunde ist verantwortlich fuer das Layout



ECU LAYOUT FOR ALL GROUP C 180DEG HEADER
Steuergeraete Layout fuer alle Header gruppe C 180 Grad
RECOMMENDED ECU LAYOUT / CUSTOMER IS RESPONSIBLE FOR LAYOUT
Empfohlenes Steuergeraete Layout / Kunde ist verantwortlich fuer das Layout



ECU LAYOUT (SYMMETRIC)
Steuergeraete Layout (symmetrisch)



ECU LAYOUT WITH POLARISATION
Steuergeraete Layout mit Polarisation

