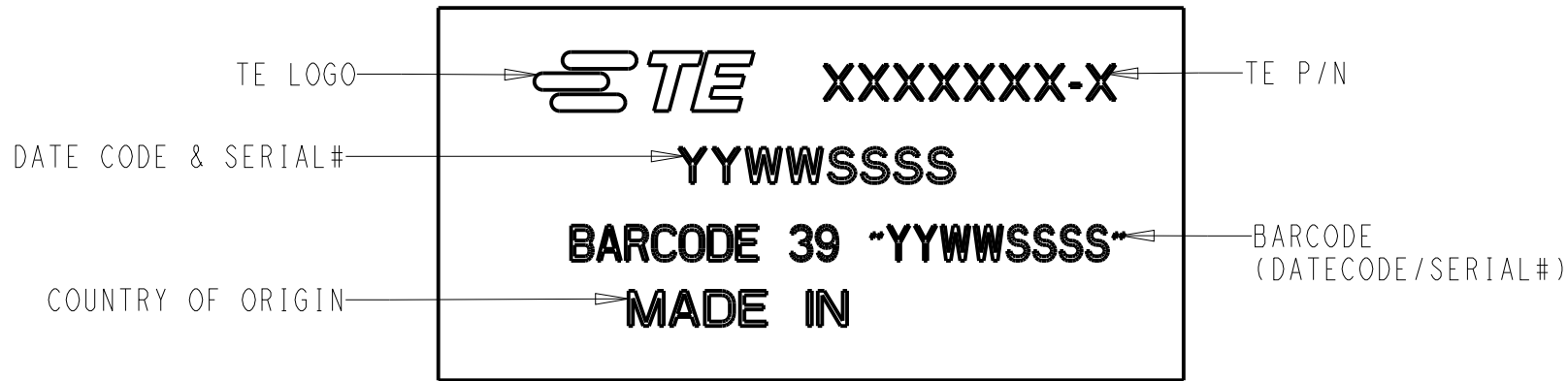


REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	A	RELEASE	22JAN2021	AC	DZ



1. ALL MATERIALS, COMPONENTS AND PROCESS SHALL COMPLY WITH TEC-138-702. (CONTAINS NO BANNED OR RESTICTED SUBSTANCES).
2. NO REACH SvHC SHALL BE CONTAINED ABOVE THE THRESHOLD AS DEFINED IN REACH SvHC COMPLIANCE DEFINITION IN ANNEX A OF TEC-138-702.
3. ASSEMBLY TO BE TESTED FOR CONTINUITY, OPENS, SHORTS, AND SIGNAL INTEGRITY.
4. CABLE BEND RADIUS 5X BUNDLED CABLE OD.
5. SEE SHEET 2 FOR WIRING SCHEMATIC.
6. ELECTRICAL PERFORMANCE MEET THE PCIe Gen4 SPEC.

 LABEL INFORMATION SHOWN BELOW:


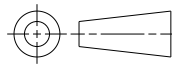


THIS PRINT IS  
**PRELIMINARY**  
UNQUALIFIED PRODUCT  
CONTACT PRODUCT ENGINEERING  
BEFORE USING THIS PRINT

1000±10	2366789-2
500±10	2366789-1
L	TE P/N

8. THE CABLE ASSEMBLY SHALL MEET IMPEDANCE REQUIREMENT:  
CONNECTORS: 85±15% ohms  
CABLE AREA: 85±10% ohms

1	PC	Low Profile slimSAS 38P Right Angle CONNECTOR	3
1	PC	Low Profile slimSAS 38P STR CONNECTOR	2
AR	M	SAS CABLE, 30AWG, 85ohm, VW-1	1
QTY	U/M	DESCRIPTION	ITEM

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN: ADAM CHEN 24OCT2019	 TE Connectivity	
DIMENSIONS:		CHK: -		
mm		APVD: DAVID ZHANG 24OCT2019	NAME: LPSS x4 30AWG STR to Right Angle 85ohm	
		PRODUCT SPEC	SIZE: A   CAGE CODE: 00779   DRAWING NO: 2366789	
		APPLICATION SPEC		
		WEIGHT: -		
		CUSTOMER DRAWING		
MATERIAL: TBD	TOLERANCES UNLESS OTHERWISE SPECIFIED:	SCALE: 6:1		SHEET: 1 OF 2
	0 PLC ±.5 1 PLC ±0.5 2 PLC ±0.13 3 PLC ±0.013 4 PLC ±0.0001 ANGLES ±.5	REV: A		


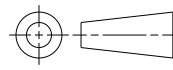
REVISIONS					
P.	LTR.	DESCRIPTION	DATE	DWN.	APVD.

P1			WIRE TYPE	P2				P1			WIRE TYPE	P2		
PIN NO.	PAIR NO.	DESIGNATION		DESIGNATION	PAIR NO.	PIN NO.		PIN NO.	PAIR NO.	DESIGNATION		DESIGNATION	PAIR NO.	PIN NO.
A1		GND		GND		B1		B1		GND		GND		A1
A2	1	PCIe TX	DIFF PAIR	PCIe TX	1	B2		B2	7	PCIe RX	DIFF PAIR	PCIe RX	7	A2
A3	1	PCIe TX	DIFF PAIR	PCIe TX	1	B3		B3	7	PCIe RX	DIFF PAIR	PCIe RX	7	A3
A4		GND		GND		B4		B4		GND		GND		A4
A5	2	PCIe TX	DIFF PAIR	PCIe TX	2	B5		B5	8	PCIe RX	DIFF PAIR	PCIe RX	8	A5
A6	2	PCIe TX	DIFF PAIR	PCIe TX	2	B6		B6	8	PCIe RX	DIFF PAIR	PCIe RX	8	A6
A7		GND		GND		B7		B7		GND		GND		A7
A8	3	SIDEBAND	DIFF PAIR	SIDEBAND	3	B8		B8	9	SIDEBAND	DIFF PAIR	SIDEBAND	9	A8
A9	3	SIDEBAND	DIFF PAIR	SIDEBAND	3	B9		B9	9	SIDEBAND	DIFF PAIR	SIDEBAND	9	A9
A10		GND		GND		B10		B10		GND		GND		A10
A11	4	SIDEBAND	DIFF PAIR	SIDEBAND	4	B11		B11	10	SIDEBAND	DIFF PAIR	SIDEBAND	10	A11
A12	4	SIDEBAND	DIFF PAIR	SIDEBAND	4	B12		B12	10	SIDEBAND	DIFF PAIR	SIDEBAND	10	A12
A13		GND		GND		B13		B13		GND		GND		A13
A14	5	PCIe TX	DIFF PAIR	PCIe TX	5	B14		B14	11	PCIe RX	DIFF PAIR	PCIe RX	11	A14
A15	5	PCIe TX	DIFF PAIR	PCIe TX	5	B15		B15	11	PCIe RX	DIFF PAIR	PCIe RX	11	A15
A16		GND		GND		B16		B16		GND		GND		A16
A17	6	PCIe TX	DIFF PAIR	PCIe TX	6	B17		B17	12	PCIe RX	DIFF PAIR	PCIe RX	12	A17
A18	6	PCIe TX	DIFF PAIR	PCIe TX	6	B18		B18	12	PCIe RX	DIFF PAIR	PCIe RX	12	A18
A19		GND		GND		B19		B19		GND		GND		A19

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THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN. ADAM CHEN 24OCT2019	 TE Connectivity		
DIMENSIONS: mm		CHK. -			
		TOLERANCES UNLESS OTHERWISE SPECIFIED:		NAME DAVID ZHANG 24OCT2019	
		0 PLC ±.5		PRODUCT SPEC	
		1 PLC ±0.13		APPLICATION SPEC	
		2 PLC ±0.13		APPROVALS	
3 PLC ±0.013		4 PLC ±0.0001		SIZE A   00779   C=2366789	
MATERIAL TBD		FINISH -		WEIGHT -	
		CUSTOMER DRAWING		SCALE 6:1 SHEET 2 OF 2 REV A	