

(C) 2019 TE Connectivity. All Rights Reserved. REVISIONS DESCRIPTION WIRE TYPE WIRE TYPE P 2 PIN NO. PAIR NO. DESIGNATION DESIGNATION | PAIR NO. | PIN NO. PIN NO. | PAIR NO. | DESIGNATION DESIGNATION PAIR NO. PIN NO. GND GND B 1 B 1 GND GND A 1 В2 B 2 PCIe TX DIFF PAIR PCIe TX PCIe RX DIFF PAIR PCIe RX Α2 13 13 А3 PCIe TX DIFF PAIR PCIe TX В3 В3 13 PCIe RX DIFF PAIR PCIe RX А3 13 A 4 GND GND B 4 B 4 GND GND A 4 DIFF PAIR B 5 B 5 DIFF PAIR PCIe RX A 5 Α5 PCIe TX PCIe TX PCIe RX 1 4 1 4 Α6 PCIe TX DIFF PAIR PCIe TX В6 B 6 1 4 PCIe RX DIFF PAIR PCIe RX 1 4 A 6 В7 В7 Α7 GND GND GND GND Α7 SIDEBAND DIFF PAIR SIDEBAND SIDEBAND SIDEBAND 8 A В8 В8 15 DIFF PAIR 8 A 15 В9 А9 SIDEBAND DIFF PAIR SIDEBAND В9 15 SIDEBAND DIFF PAIR SIDEBAND 15 А9 A 1 0 GND GND B 1 0 B10 GND GND A 1 0 SIDEBAND DIFF PAIR SIDEBAND B 1 1 SIDEBAND DIFF PAIR A 1 1 B 1 1 16 SIDEBAND 16 A 1 1 B12 A12 A 1 2 SIDEBAND DIFF PAIR SIDEBAND B12 16 SIDEBAND DIFF PAIR SIDEBAND 16 A13 GND GND B13 B13 GND GND A 1 3 PCIe TX DIFF PAIR B 1 4 DIFF PAIR PCIe RX A 1 4 PCIe TX B14 1 7 PCIe RX A 1 4 1 7 A 1 5 PCIe TX DIFF PAIR PCIe TX B15 B15 1 7 PCIe RX DIFF PAIR PCIe RX 1 7 A 1 5 A 1 6 GND GND B16 B16 GND GND A 1 6 A 1 7 PCIe TX DIFF PAIR B 1 7 B 1 7 PCIe RX DIFF PAIR PCIe RX A 1 7 PCIe TX 18 18 A18 PCIe TX DIFF PAIR PCIe TX B18 B18 18 PCIe RX DIFF PAIR PCIe RX 18 A18 A19 GND GND B19 B19 GND GND A 1 9 A20 PCIe TX DIFF PAIR PCIe TX B20 B20 PCIe RX DIFF PAIR PCIe RX 19 19 A 2 0 A 2 1 PCIe TX DIFF PAIR PCIe TX B 2 1 B 2 1 DIFF PAIR PCIe RX 19 A 2 1 19 PCIe RX A 2 2 GND GND B22 B22 GND GND A 2 2 DIFF PAIR A23 PCIe TX PCIe TX B23 B23 PCIe RX DIFF PAIR PCIe RX A 2 3 20 20 A 2 4 PCIe TX DIFF PAIR B 2 4 B 2 4 20 DIFF PAIR PCIe RX 20 A 2 4 PCIe TX PCIe RX A 2 5 GND GND B25 B25 GND GND A 2 5 A26 SIDEBAND DIFF PAIR SIDEBAND B26 B26 21 SIDEBAND DIFF PAIR SIDEBAND 2 1 A 2 6 SIDEBAND SIDEBAND SIDEBAND A 2 7 SIDEBAND DIFF PAIR B27 B 2 7 DIFF PAIR A 2 7 21 A28 GND GND B28 B28 GND GND A28 SIDEBAND DIFF PAIR SIDEBAND DIFF PAIR SIDEBAND B29 SIDEBAND 22 A29 A 2 9 10 B29 22 SIDEBAND SIDEBAND DIFF PAIR B30 B30 SIDEBAND DIFF PAIR SIDEBAND A30 A 3 1 A 3 1 GND GND B 3 1 B 3 1 GND GND A32 PCIe TX DIFF PAIR PCIe TX B32 B32 DIFF PAIR PCIe RX A32 23 PCIe RX 23 A 3 3 PCIe TX DIFF PAIR B33 B33 DIFF PAIR PCIe RX 1 1 PCIe TX 1 1 PCIe RX А33 23 A 3 4 GND GND B34 B34 GND GND A 3 4 A35 PCIe TX DIFF PAIR PCIe TX B35 PCIe RX DIFF PAIR PCIe RX A35 12 B35 24 2 4 A36 PCIe TX DIFF PAIR 12 В36 B36 PCIe RX DIFF PAIR PCIe RX A36 PCIe TX 24 24 А37 GND GND B37 B 3 7 GND GND A 3 7 THIS DRAWING IS A CONTROLLED DOCUMENT. TE Connectivity CA, LPSS x8 30AWG STR to RA 850hm APPLICATION SPEC ±0.0001 | 00779 C-2366784 TBD JSTOMER DRAWING 4805 (3/13)