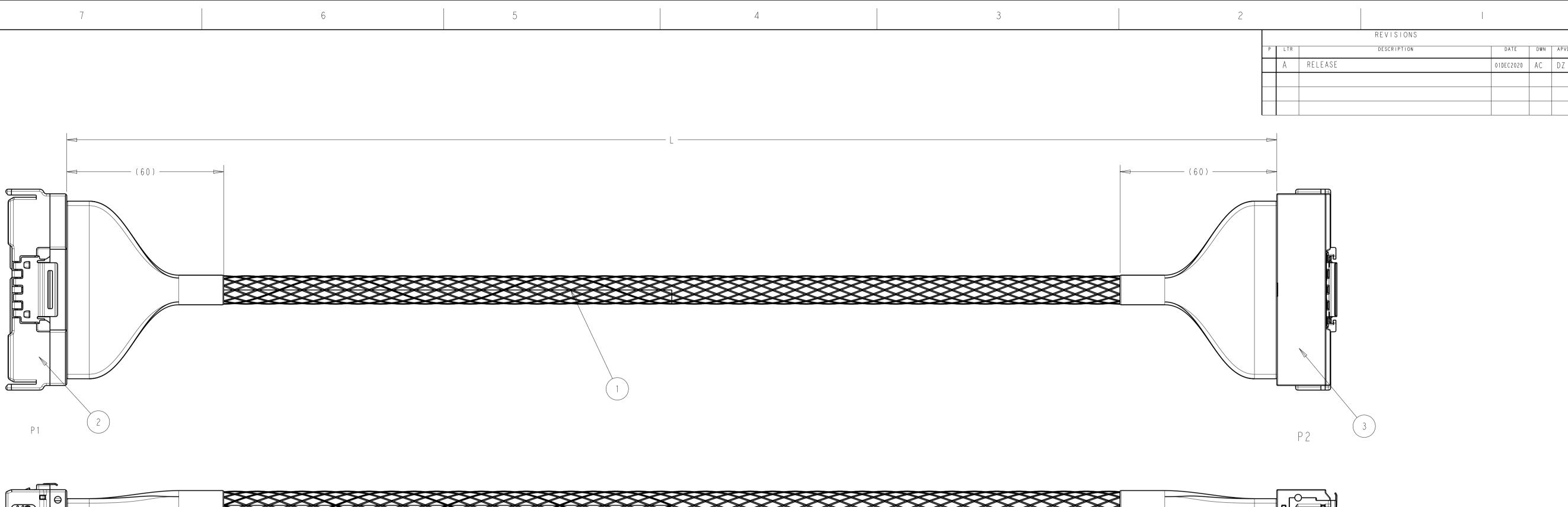
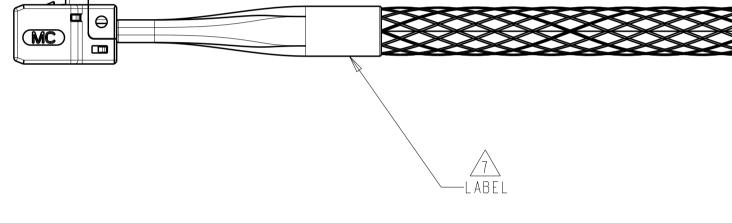


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- 1. ALL MATERIALS. COMPONENTS AND PROCESS SHALL COMPLY WITH TEC-138-702. (CONTAINS NO BANNED OR RESTICTED SUBSTANCES).
- 2. NO REACH SVHC SHALL BE CONTAINED ABOVE THE THRESHOLD AS DEFINED IN REACH SVHC COMPLIANCE DEFINITION IN ANNEX A OF TEC-138-702.
- 3. ASSEMBLY TO BE TESTED FOR CONTINUITY, OPENS, SHORTS, AND SIGNAL INTEGRITY.
- 4. CABLE BEND RADIUS 5X BUNDLED CABLE OD.
- 5. SEE SHEET 2 FOR WIRING SCHEMATIC.
- 6. ELECTRICAL PERFORMANCE MEET THE PCIe Gen4 SPEC.

A LABEL INFORMATION SHOWN BELOW:

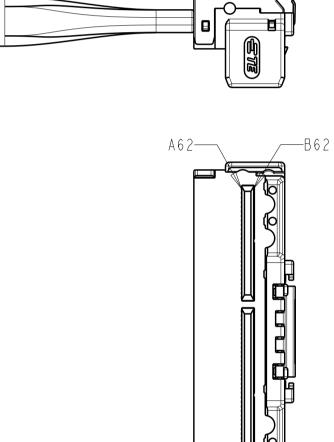
4805 (3/|3)



8. THE CABLE ASSEMBLY SHALL MEET IMPEDANCE REQUIREMENT: CONNECTORS: 85±15% ohms CABLE AREA: 85±10% ohms



THIS DRAWING IS A DIMENSIONS: mm ТВD



1000±10	2366783-2
500±10	2366783-1
L	TE P/N

		1	РC	MINI COOLEDGE	124P Right Angle	CONNECTOR		3		
		1	РC	MINI COOLEDGE	124P STR CONNECT	OR		2		
		AR	М	SAS CABLE, 30,	AWG, 850hm, VW-1			1		
		QTY	U/M		DESCRIPTION			ΙΤΕΜ		
			DWN	240072010						
С	ONTROLLED DO	OCUMENT.	DWN <u>ADAM</u> снк	240CT2019 <u>CHEN</u> -	=7	E TE	Connectivit	у		
TOLERANCES UNLESS OTHERWISE SPECIFIED: APVD 240CT2019 DAVID ZHANG			240CT2019 D ZHANG	NAME CA, MCIO x16	30AWG					
7	0 PLC ±- 1 PLC ±0. 2 PLC ±0.		PRODUCT	SPEC	STR to RA					
3 PLC ±0.013 AP		APPLICA	TION SPEC	850hm						
	4 PLC ±0. ANGLES	0001 ±-	-		SIZE CAGE CODE DRAWING N			RESTRICTED TO		
	FINISH -		WEIGHT	-	A 00779 C - 23	66783		-		
	-		CUSTO	MER DRAWING		scale 6:1	SHEET OF 2	REVA		

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	P 1		WIER TYPE		P 2			P 1		WIER TYPE		P 2	
PIN NO.	PAIR NO.	DESIGNATION		DESIGNATION	PAIR NO.	PIN NO.	PIN NO.	PAIR NO.	DESIGNATION		DESIGNATION	PAIR NO.	PIN NO.
A 1		GND		GND		B 1	B 1		GND		GND		A 1
A 2	1	PCIe TX	DIFF PAIR	PCIe TX	1	В2	В2	21	PCIe RX	DIFF PAIR	PCIe RX	21	A 2
A 3	1	PCIe TX	DIFF PAIR	PCIe TX	1	В 3	В3	21	PCIe RX	DIFF PAIR	PCIe RX	21	A 3
A 4		GND		GND		B 4	B 4		GND		GND		A 4
A 5	2	PCIe TX	DIFF PAIR	PCIe TX	2	В 5	B 5	22	PCIe RX	DIFF PAIR	PCIe RX	22	A 5
A 6	2	PCIe TX	DIFF PAIR	PCIe TX	2	B6	B 6	22	PCIe RX	DIFF PAIR	PCIe RX	22	A 6
A 7		GND		GND	-	B 7	B 7		GND		GND		A 7
A 8	3	SIDEBAND	DIFF PAIR	SIDEBAND	3	B8	<u> </u>	23	SIDEBAND	DIFF PAIR	SIDEBAND	23	A 8
A 9	3	SIDEBAND	DIFF PAIR	SIDEBAND	3	B9	B9	23	SIDEBAND	DIFF PAIR	SIDEBAND	23	A 9
A 1 0 A 1 1	1	GND SIDEBAND	DIFF PAIR	GND SIDEBAND	4	B10 B11	B10 B11	24	GND SIDEBAND	DIFF PAIR	GND SIDEBAND	2 4	A 1 0 A 1 1
A11 A12	4	SIDEBAND	DIFF FAIR DIFF PAIR	SIDEBAND	4	B12	B12	24	SIDEBAND	DIFF PAIR	SIDEBAND	2 4	ATT ATZ
A 1 3	4	GND		GND	4	B13	B13	L 4	GND	DITT TAIN	GND	<u> </u>	A 1 3
A 1 4	5	PCIe TX	DIFF PAIR	PCIe TX	5	B14	B14	25	PCIe RX	DIFF PAIR	PCIe RX	25	A 1 4
A 1 5	5	PCIe TX	DIFF PAIR	PCIe TX	5	B15	B15	25	PCIe RX	DIFF PAIR	PCIe RX	2 5	A 1 5
A 1 6		GND		GND		B16	B16		GND		GND		A 1 6
A 1 7	6	PCIe TX	DIFF PAIR	PCIe TX	6	B17	B17	26	PCIe RX	DIFF PAIR	PCIe RX	26	A 1 7
A 1 8	6	PCIe TX	DIFF PAIR	PCIe TX	6	B18	B18	26	PCIe RX	DIFF PAIR	PCIe RX	26	A 1 8
A 1 9		GND		GND		B19	B19		GND		GND		A 1 9
A 2 0	7	PCIe TX	DIFF PAIR	PCIe TX	7	B20	B 2 0	27	PCIe RX	DIFF PAIR	PCIe RX	27	A 2 0
A 2 1	7	PCIe TX	DIFF PAIR	PCIe TX	7	B 2 1	B 2 1	27	PCIe RX	DIFF PAIR	PCIe RX	27	A 2 1
A 2 2		GND		GND		B 2 2	B 2 2		GND		GND		A 2 2
A 2 3	8	PCIe TX	DIFF PAIR	PCIe TX	8	B23	B 2 3	28	PCIe RX	DIFF PAIR	PCIe RX	28	A 2 3
A 2 4	8	PCIe TX	DIFF PAIR	PCIe TX	8	B 2 4	B 2 4	28	PCIe RX	DIFF PAIR	PCIe RX	28	A 2 4
A 2 5		GND		GND		B 2 5	B25	2.0	GND		GND		A 2 5
A 2 6	9	SIDEBAND	DIFF PAIR	SIDEBAND	9	B26	B26	29	SIDEBAND	DIFF PAIR	SIDEBAND	29	A 2 6
A 2 7	9	SIDEBAND	DIFF PAIR	SIDEBAND	9	B27	B27	29	SIDEBAND	DIFF PAIR	SIDEBAND	29	A 2 7
A 2 8 A 2 9	1 0	GND SIDEBAND	DIFF PAIR	GND SIDEBAND	1 0	B28 B29	B28 B29	30	GND SIDEBAND	DIFF PAIR	GND SIDEBAND	30	A 2 8 A 2 9
A 3 0	10	SIDEBAND	DIFF PAIR	SIDEBAND	10	B30	B30	30	SIDEBAND	DIFF PAIR	SIDEBAND	30	A 2 9
A 3 1	10	GND		GND		B 3 1	B 3 1	50	GND		GND		A 3 1
A 3 2	11	PCIe TX	DIFF PAIR	PCIe TX	11	B32	B 3 2	31	PCIe RX	DIFF PAIR	PCIe RX	31	A 3 2
A 3 3	1 1	PCIe TX	DIFF PAIR	PCIe TX	1 1	B 3 3	B 3 3	31	PCIe RX	DIFF PAIR	PCIe RX	31	A 3 3
A 3 4		GND		GND		B 3 4	B34		GND		GND		A 3 4
A 3 5	1 2	PCIe TX	DIFF PAIR	PCIe TX	1 2	B 3 5	B35	32	PCIe RX	DIFF PAIR	PCIe RX	32	A 3 5
A 3 6	1 2	PCIe TX	DIFF PAIR	PCIe TX	1 2	B36	B36	32	PCIe RX	DIFF PAIR	PCIe RX	32	A 3 6
A 3 7		GND		GND		В37	B 3 7		GND		GND		A 3 7
		KEY		KEY					KEY		KEY		
A 3 8		GND		GND		B38	B38		GND		GND		A 3 8
A 3 9	1 3	PCIe TX	DIFF PAIR	PCIe TX	1 3	B 3 9	B 3 9	33	PCIe RX	DIFF PAIR	PCIe RX	33	A 3 9
A 4 0	1 3	PCIe TX	DIFF PAIR	PCIe TX	1 3	B 4 0	B 4 0	33	PCIe RX	DIFF PAIR	PCIe RX	33	A 4 0
A 4 1		GND		GND		B 4 1	B 4 1	2.4	GND		GND	2.4	A 4 1
A 4 2	1 4	PCIe TX	DIFF PAIR	PCIe TX	1 4	B 4 2	B 4 2	34	PCIe RX	DIFF PAIR	PCIe RX	3.4	A 4 2
A 4 3 A 4 4	14	PCIe TX GND	DIFF PAIR	PCIe TX GND	1 4	B 4 3 B 4 4	B 4 3 B 4 4	34	<u> </u>	DIFF PAIR	PCIeRX GND	34	A 4 3 A 4 4
A 4 4	1 5	PCIe TX	DIFF PAIR	PCIe TX	1 5	B 4 5	B 4 5	35	PCIe RX	DIFF PAIR	PCIe RX	35	A 4 4
A 4 5	15	PCIe TX	DIFF PAIR	PCIe TX	15	B45 B46	B45	35	PCIe RX	DIFF PAIR	PCIe RX	35	A 4 6
A 4 7		GND		GND		B 4 7	B 4 7		GND		GND	0.0	A 4 7
A 4 8	16	PCIe TX	DIFF PAIR	PCIe TX	16	B48	B 4 8	36	PCIe RX	DIFF PAIR	PCIe RX	36	A 4 8
A 4 9	1 6	PCIe TX	DIFF PAIR	PCIe TX	1 6	B 4 9	B 4 9	36	PCIe RX	DIFF PAIR	PCIe RX	36	A 4 9
A 5 0		GND		GND		B 5 0	B 5 0		GND		GND		A 5 0
A 5 1	17	PCIe TX	DIFF PAIR	PCIe TX	17	B 5 1	B 5 1	37	PCIe RX	DIFF PAIR	PCIe RX	37	A 5 1
A 5 2	17	PCIe TX	DIFF PAIR	PCIe TX	17	B 5 2	B52	37	PCIe RX	DIFF PAIR	PCIe RX	37	A 5 2
A 5 3		GND		GND		B 5 3	B 5 3		GND		GND		A 5 3
A 5 4	18	PCIe TX	DIFF PAIR	PCIe TX	18	B 5 4	B 5 4	38	PCIe RX	DIFF PAIR	PCIe RX	38	A 5 4
A 5 5	18	PCIe TX	DIFF PAIR	PCIe TX	18	B 5 5	B 5 5	38	PCIe RX	DIFF PAIR	PCIe RX	38	A 5 5
A 5 6		GND		GND		B 5 6	B 5 6		GND		GND		A 5 6
A 5 7	19	PCIe TX	DIFF PAIR	PCIe TX	19	B 5 7	B 5 7	39	PCIe RX	DIFF PAIR	PCIe RX	39	A 5 7
A 5 8	19	PCIe TX	DIFF PAIR	PCIe TX	19	B 5 8	B 5 8	39	PCIe RX	DIFF PAIR	PCIe RX	39	A 5 8
A 5 9	0.0	GND		GND	0.0	B 5 9	B 5 9	4.0	GND		GND	4.0	A 5 9
A 6 0	20	PCIe TX	DIFF PAIR	PCIe TX	20	B60	B60	40	PCIe RX	DIFF PAIR	PCIe RX	40	A 6 0
A 6 1	20	PCIe TX	DIFF PAIR	PCIe TX	20	B61	B61 B62	40	PCIe RX	DIFF PAIR	PCIe RX	40	A 6 1
A 6 2		GND		GND		B62	DVZ		GND		GND		A 6 2

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THIS DRAWING IS A CO DIMENSIONS: mm MATERIAL TBD

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С	ONTROLLED DOCUMENT.	dwn 240СТ2019 ADAM CHEN снк -	TE Connectivity
	TOLERANCES UNLESS OTHERWISE SPECIFIED:	APVD 240CT2019 DAVID ZHANG PRODUCT SPEC	NAME CA, MCIO x16 30AWG
1	0 PLC ±- I PLC ±0.5 2 PLC ±0.13 3 PLC ±0.013	- APPLICATION SPEC	STR to RA 850hm
	4 PLC ±0.0001 ANGLES ±-	-	SIZE CAGE CODE DRAWING NO RESTRICTED TO
	FINISH -	WEIGHT _	A 00779 C-2366783 -
	-	CUSTOMER DRAWING	SCALE 6:1 SHEET 2 2 REV A

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