

(C) 2019 TE Connectivity. All Rights Reserved. REVISIONS DESCRIPTION DATE DWN APVD **WIER TYPE** P2 P1 **WIER TYPE** P2 PAIR NO. PIN NO. PIN NO. PAIR NO. DESIGNATION DESIGNATION PIN NO. PAIR NO. DESIGNATION DESIGNATION PAIR NO. PIN NO. B1 B1 Α1 GND GND GND GND A2 PCIe TX DIFF PAIR PCIe TX B2 B2 PCIe RX DIFF PAIR PCle RX 21 A2 1 1 21 DIFF PAIR PCIe TX 1 B3 B3 21 DIFF PAIR PCIe RX 21 A3 1 PCIe TX PCle RX GND B4 A4 GND GND B5 2 PCIe TX DIFF PAIR PCIe TX 2 B5 PCle RX DIFF PAIR PCle RX 22 A5 22 B6 PCIe TX DIFF PAIR PCIe TX 2 B6 22 PCIe RX DIFF PAIR PCIe RX 22 A6 B7 Α7 Α7 B7 GND GND GND GND SIDEBAND DIFF PAIR SIDEBAND B8 B8 SIDEBAND DIFF PAIR SIDEBAND 23 **A8** 3 3 23 3 SIDEBAND DIFF PAIR SIDEBAND 3 B9 B9 23 SIDEBAND DIFF PAIR SIDEBAND 23 Α9 B10 B10 A10 GND GND GND B11 A11 SIDEBAND DIFF PAIR SIDEBAND 4 B11 24 SIDEBAND DIFF PAIR SIDEBAND 24 A11 4 A12 SIDEBAND DIFF PAIR SIDEBAND B12 B12 24 SIDEBAND DIFF PAIR SIDEBAND 24 A12 4 4 B13 GND GND B13 GND GND A13 DIFF PAIR PCIe TX B14 B14 DIFF PAIR PCIe RX A14 5 PCIe TX 5 PCle RX 25 A14 25 A15 5 PCIe TX DIFF PAIR PCIe TX 5 B15 B15 25 PCIe RX DIFF PAIR PCIe RX 25 A15 GND B16 B16 A16 GND GND A17 PCIe TX DIFF PAIR PCIe TX B17 B17 DIFF PAIR PCIe RX A17 6 26 PCle RX 26 6 A18 6 PCIe TX DIFF PAIR PCIe TX B18 B18 26 PCIe RX DIFF PAIR PCle RX 26 A18 6 GND GND B19 B19 GND A19 GND DIFF PAIR PCIe TX B20 B20 DIFF PAIR PCIe RX 27 A20 7 PCIe TX 27 PCle RX A21 7 PCIe TX DIFF PAIR PCIe TX 7 B21 B21 PCIe RX DIFF PAIR PCIe RX 27 A21 27 B22 B22 A22 A22 GND GND GND GND A23 DIFF PAIR PCIe TX B23 B23 DIFF PAIR PCIe RX A23 PCIe TX 8 PCle RX 28 8 B24 A24 8 PCIe TX DIFF PAIR PCIe TX 8 B24 28 PCIe RX DIFF PAIR PCIe RX 28 A24 B25 B25 GND GND GND A25 GND A26 9 SIDEBAND DIFF PAIR SIDEBAND 9 B26 B26 SIDEBAND DIFF PAIR SIDEBAND 29 A26 29 A27 9 SIDEBAND DIFF PAIR SIDEBAND 9 B27 B27 SIDEBAND DIFF PAIR SIDEBAND 29 A27 29 GND B28 B28 A28 GND GND GND B29 A29 SIDEBAND DIFF PAIR SIDEBAND B29 SIDEBAND DIFF PAIR SIDEBAND A29 10 10 30 30 A30 SIDEBAND DIFF PAIR SIDEBAND 10 B30 B30 SIDEBAND DIFF PAIR SIDEBAND 30 A30 10 30 B31 A31 GND B31 GND A31 GND A32 DIFF PAIR PCIe TX B32 B32 DIFF PAIR A32 11 PCIe TX PCle RX PCIe RX 31 11 31 DIFF PAIR DIFF PAIR B33 A33 A33 11 PCIe TX PCIe TX 11 B33 31 PCIe RX PCIe RX 31 B34 GND B34 GND A34 GND GND DIFF PAIR PCIe TX B35 B35 DIFF PAIR A35 A35 12 PCIe TX 12 32 PCle RX PCIe RX 32 A36 DIFF PAIR PCIe TX B36 B36 DIFF PAIR PCIe RX A36 12 PCIe TX 12 32 PCle RX 32 A37 GND GND B37 B37 GND GND A37 KEY KEY KEY KEY A38 GND GND B38 B38 GND GND A38 A39 DIFF PAIR B39 B39 DIFF PAIR A39 13 PCIe TX PCIe TX 13 33 PCle RX PCIe RX 33 A40 13 PCIe TX DIFF PAIR PCIe TX 13 B40 B40 33 PCIe RX DIFF PAIR PCIe RX 33 A40 B41 B41 A41 GND GND GND GND DIFF PAIR PCIe TX B42 B42 PCIe RX DIFF PAIR PCIe RX 34 A42 14 PCIe TX 14 A43 DIFF PAIR B43 B43 DIFF PAIR 34 14 PCIe TX 14 34 PCIe RX A43 PCIe TX PCle RX GND B44 B44 GND A44 GND GND B45 A45 DIFF PAIR B45 DIFF PAIR A45 15 PCIe TX PCIe TX 15 35 PCle RX PCIe RX 35 B46 B46 A46 15 DIFF PAIR PCIe TX 15 DIFF PAIR PCIe RX 35 A46 PCIe TX 35 PCle RX GND B47 B47 GND A47 GND GND B48 DIFF PAIR PCIe TX B48 DIFF PAIR A48 16 PCIe TX 16 36 PCIe RX PCIe RX 36 DIFF PAIR B49 B49 36 16 PCIe TX PCIe TX 16 36 PCIe RX DIFF PAIR PCIe RX A49 A50 B50 B50 A50 GND GND GND GND A51 17 PCIe TX DIFF PAIR PCIe TX 17 B51 B51 37 PCIe RX DIFF PAIR PCIe RX 37 A51 17 B52 B52 37 A52 A52 DIFF PAIR PCIe TX 17 37 DIFF PAIR PCIe RX PCIe TX PCle RX A53 GND GND B53 B53 GND GND A53 DIFF PAIR DIFF PAIR B54 B54 A54 18 PCIe TX PCIe TX 18 38 PCle RX PCIe RX 38 A55 18 DIFF PAIR PCIe TX 18 B55 B55 38 DIFF PAIR 38 A55 PCIe TX PCle RX PCIe RX B56 B56 A56 A56 GND GND GND GND B57 B57 A57 A57 19 PCIe TX DIFF PAIR PCIe TX 19 39 PCIe RX DIFF PAIR PCIe RX 39 B58 B58 A58 A58 DIFF PAIR PCIe TX 19 PCIe RX DIFF PAIR PCIe RX 39 19 PCIe TX 39 A59 B59 B59 A59 GND GND GND GND B60 B60 A60 20 PCIe TX DIFF PAIR PCIe TX 20 PCle RX DIFF PAIR PCIe RX 40 A60 A61 B61 B61 A61 20 PCIe TX DIFF PAIR 20 PCIe RX DIFF PAIR PCIe RX 40 PCIe TX A62 B62 B62 A62 GND GND GND GND ADAM CHEN THIS DRAWING IS A CONTROLLED DOCUMENT. TE Connectivity DIMENSIONS: 24OCT2019 DAVID ZHANG CA, MCIO x16 30AWG Lateral

4805 (3/13)

85ohm

SIZE CAGE CODE DRAWING NO

±0.13 ±0.013

 ± 0.0001

APPLICATION SPEC

CUSTOMER DRAWING

2 PLC 3 PLC 4 PLC

TBD

ANGLES