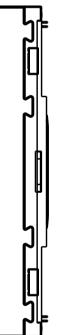


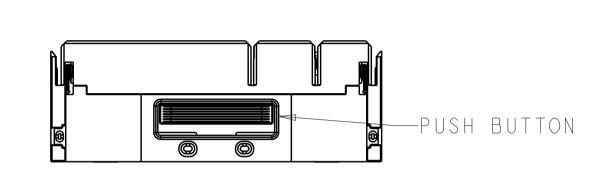
4805 (3/13)

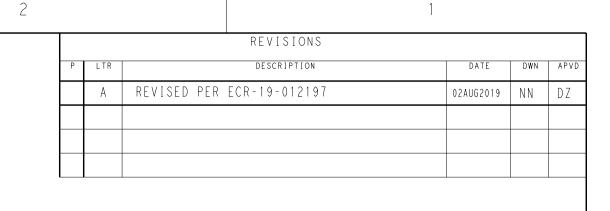
THIS	DRAWING	ΙS	А
	DIMENSIONS	:	
	mm		
	÷		
	Ψ		
MATERIAL			
MATERIAL			
	-		
	-		

	500	1000 + 10/ - 0	2340885-2
	250	500 +10/-0	2340885-1
	(" M ")	"L"	TE P/N
CONTROLLED DOCUMENT.	DWN 06AUG2018 B. MATTHEWS CHK 06AUG2018	€ TE	TE Connectivity
TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±- 1 PLC ±0.5 2 PLC ±0.13 3 PLC ±0.013 4 PLC ±0.0001	G. GUTGOLD	ME SLIVER 2.0 4C 30AV RIGHT ANGLE TO RIC -	
ANGLES ±- FINISH		$\frac{1}{1} \begin{array}{ } 0 \ 0 \ 7 \ 7 \ 9 \end{array} = \begin{array}{ } 0 \ 0 \ -2 \ 3 \ 4 \ 0 \ 8 \ 5 \end{array}$	
-	CUSTOMER DRAWING	SCALE	3:2 SHEET 1 OF 3 REV A



P2





В

8 7	6	5		4			3	
THIS DRAWING IS UNPUBLISHED. RELEASED FOR PUBLICATION 20 C COPYRIGHT 20 BY - ALL RIGHTS RESERVED.		P1				P2		
		PIN NO. PAIR NO.	DESIGNATION	WIRE TYPE	DESIGNATION	PAIR NO.	PIN NO.	
			GROUND	DISCRETE	GROUND		A 1	
		A 2 A 3	GROUND GROUND	DISCRETE DISCRETE	GROUND GROUND		A 2 A 3	
	COMMONED —	A 4	GROUND	DISCRETE	GROUND		A 4	COMMONED
		A 5 A 6	GROUND	DISCRETE	GROUND		A 5 A 6	
		A 7	GROUND SIDE BAND	DISCRETE DISCRETE	GROUND SIDE BAND		A 7	
		A 8	SIDE BAND	DISCRETE	SIDE BAND		A 8	
		A 9 A 1 0	SIDE BAND SIDE BAND	DISCRETE DISCRETE	SIDE BAND SIDE BAND		A 9 A 1 0	
		A 1 1	SIDE BAND	DISCRETE	SIDE BAND		A 1 0	
		A 1 2	SIDE BAND	DISCRETE	SIDE BAND		A 1 2	
		A 1 3 A 1 4	G N D C L K	DIFF PAIR	G N D C L K		A 1 3 A 1 4	
		A 1 5	CLK	DIFF PAIR	CLK		A 1 5	
		A 1 6	GND		GND		A 1 6	
		A 1 7 0 A 1 8 0	PCIeTX	DIFF PAIR DIFF PAIR	PCIeTX PCIe TX	0	A 1 7 A 1 8	
		A 1 9	PCIe TX GND		GND		A 1 9	
		A 2 0 1	РСІеТХ	DIFF PAIR	РСІеТХ	1	A 2 0	
		A 2 1 1	PCIe TX	DIFF PAIR	PCIe TX	1	A 2 1	
		A 2 2 A 2 3 2	G N D PCIeTX	DIFF PAIR	GND PCIeTX	2	A 2 2 A 2 3	
		A 2 4 2	PCIe TX	DIFF PAIR	PCIe TX	2	A 2 4	
		A 2 5	GND		GND	3	A 2 5 A 2 6	
		A 2 6 3 A 2 7 3	PCIeTX PCIe TX	DIFF PAIR DIFF PAIR	PCIeTX PCIe TX	3	A 2 6 A 2 7	
		A 2 8	GND		GND		A 2 8	
		A 2 9	<u> </u>		K E Y G N D		A 2 9	
		A 3 0 4	PCIeTX	DIFF PAIR	PCIeTX	4	A 2 9 A 3 0	
		A 3 1 4	PCIe TX	DIFF PAIR	PCIe TX	4	A 3 1	
		A 3 2	GND	DIFF PAIR	GND	5	A 3 2 A 3 3	
		A 3 3 5 A 3 4 5	PCIeTX PCIe TX	DIFF PAIR	PCIeTX PCIe TX	5	A 3 4	
		A 3 5	GND		GND		A 3 5	
		A 3 6 6 A 3 7 6	PCIeTX PCIe TX	DIFF PAIR DIFF PAIR	PCIeTX	6	A 3 6 A 3 7	
		A 3 8	GND		PCIeTX GND	0	A 3 7 A 3 8	
		A 3 9 7	РСІеТХ	DIFF PAIR	РСІеТХ	7	A 3 9	
		A 4 0 7 A 4 1	<u>PCIeTX</u> GND	DIFF PAIR	PCIeTX GND	7	A 4 0 A 4 1	
		A 4 2	SIDE BAND	DISCRETE	SIDE BAND		A 4 1 A 4 2	
			KEY		KEY			
		A 4 3 A 4 4 8	G N D PCIeTX	DIFF PAIR	GND PCIeTX	8	A 4 3 A 4 4	
B		A 4 4 8 A 4 5 8	PCIeTX PCIe TX	DIFF PAIR	PCIeTX	8	A 4 4 A 4 5	
		A 4 6	GND		GND		A 4 6	
		A 4 7 9 A 4 8 9	PCIeTX PCIe TX	DIFF PAIR DIFF PAIR	PCIeTX PCIe TX	9	A 4 7 A 4 8	
		A 4 9	GND		GND		A 4 9	
		A 5 0 1 0	РСІеТХ	DIFF PAIR	РСІеТХ	10	A 5 0	
		A 5 1 1 0 A 5 2	PCIe TX GND	DIFF PAIR	PCIeTX GND	10	A 5 1 A 5 2	
		A 5 3 1 1	PC I e T X	DIFF PAIR	PCIeTX	1 1	A 5 3	
		A 5 4 1 1	PCIe TX	DIFF PAIR	PCIe TX	1 1	A 5 4	
		A 5 5 A 5 6 1 2	GND PCIeTX	DIFF PAIR	GND PCIeTX	1 2	A 5 5 A 5 6	
		A 5 7 1 2	PCIe TX			1 2	A 5 7	
		A 5 8	GND		GND		A 5 8	
		A 5 9 1 3 A 6 0 1 3	PCIeTX PCIe TX	DIFF PAIR DIFF PAIR	PCIeTX PCIe TX	13	A 5 9 A 6 0	
		A 6 1	GND		GND		A 6 1	
		A 6 2 1 4	PCIeTX	DIFF PAIR	PCIeTX	1 4	A 6 2	
A		A 6 3 1 4 A 6 4	<u>PCIeTX</u> GND	DIFF PAIR	PCIeTX GND	1 4	A 6 3 A 6 4	
		A 6 5 1 5	PC I e T X	DIFF PAIR	PCIeTX	1 5	A 6 5	THIS DRAWING IS A CONTROLLED DOCUMENT
		A66 15	PCIe TX	DIFF PAIR	PCIe TX	1 5	A 6 6	DIMENSIONS: TOLERANCES UNLESS OTHERWISE SPECIFIED:
		A 6 7 A 6 8	GND RESERVED	DISCRETE	GND RESERVED		A 6 7 A 6 8	0 PLC ±-
		A 6 9	RESERVED	DISCRETE	RESERVED		A 6 9	2 PLC ±0.13 3 PLC ±0.013 4 PLC ±0.0001
		A 7 0	RESERVED	DISCRETE	RESERVED		A 7 0	ANGLES ±- MATERIAL FINISH

4805 (3/13)

		REVISIONS			
Ρ	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-

1

(
1	/	

А

DWN 06AUG2018 <u>В. MATTHEWS</u> снк 06AUG2018 <u>G. GUTGOLD</u> IS A CONTROLLED DOCUMENT. TE Connectivity TOLERANCES UNLESS OTHERWISE SPECIFIED: PVD AME SLIVER 2.0 4C 30AWG RIGHT ANGLE TO RIGHT ANGLE 0 PLC ±-1 PLC ±0.5 2 PLC ±0.13 3 PLC ±0.013 4 PLC ±0.0001 ANGLES +-RODUCT SPEC APPLICATION SPEC size cage code drawing no A 1 0 0 7 7 9 C - 2 3 4 0 8 8 5 RESTRICTED TO INISH IGHT -SCALE 1:1 SHEET OF 3 REV A CUSTOMER DRAWING -

PH (0) PH (0) <th>THIS DRAWING IS UNPUBLISHED. RELEASED FOR PUBLICATION 20 C COPYRIGHT 20 BY - ALL RIGHTS RESERVED.</th> <th></th> <th></th> <th> P1</th> <th></th> <th></th> <th></th> <th>P2</th> <th></th> <th></th> <th>P</th>	THIS DRAWING IS UNPUBLISHED. RELEASED FOR PUBLICATION 20 C COPYRIGHT 20 BY - ALL RIGHTS RESERVED.			 P1				P2			P
			PIN NO. F		DESIGNATION	WIRE TYPE			PIN NO.		
		COMMONED —	- ВЗ		12V POWER	DISCRETE	12V POWER		B 3 -	COMMONED	
			В 5			DISCRETE			B 5 -	_	
$ \begin{vmatrix} 33 \\ 3 \\ 3 \\ 3 \\ 3 \\ 4 \\ 4 \\ 4 \\ 4 \\ 4 $		L									
$ \begin{vmatrix} 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1$			B 8		SIDE BAND	DISCRETE	SIDE BAND		B 8		
1 1 1 1 1 1 1 1 1 1 2 2 2 1											
$ \begin{array}{cccccccccccccccccccccccccccccccccccc$											
HD I = 1 <thi 1<="" =="" th=""> <thi 1<="" =="" th=""> <thi 1<<="" =="" th=""><th></th><th></th><th>B13</th><th></th><th>GND</th><th></th><th>GND</th><th></th><th>B13</th><th></th><th></th></thi></thi></thi>			B13		GND		GND		B13		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $			B15						B15		
$ \begin{vmatrix} 0.22 & 0.2 & $				<u> </u>		DIFF PAIR					
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $			B18	-	PCIe RX		PCIe RX		B18		
$ \begin{vmatrix} 872 \\ 382 \\ 3$				1		DIFF PAIR		1			
23 2 X1-4 X2 X2 X2 X2 X2 24 - X2-4 X2 X2 X2 X2 X2 24 - X2-4 X2 X2 X2 X2 X2 X2 242 - X2-4 X2			B 2 1	1	PCIe RX		PCIe RX	1	B 2 1		
			B 2 3	2	PCIe RX		PCIe RX		B23		
2:30 3: 3:50 3: 1:				2		DIFF PAIR		2			
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $			B 2 6	3	PCIe RX		PCIe RX		B26		
2:0 340 340 040 043 2:3 4 66 31 11 12 4 5 2:3 4 62 52 127 74 12 12 5 2:3 4 74 52 52 127 74 12 12 5 027 2:4 5 75 75 75 75 75 75 75 75 75 75 2:4 5 75 75 75 75 75 75 75 75 75 2:4 5 75 75 75 75 75 75 75 75 2:5 6 75 75 75 75 75 75 75 2:5 6 75 75 75 75 75 75 75 2:5 7 75 75 75 75 75 75 75 2:5 7 75 75 75 75 75 75 75 2:6 7 75 75 75 75 75 75 75 2:7 75 75 75 75 </th <th></th> <th></th> <th></th> <th><u></u>З</th> <th>GND</th> <th></th> <th>GND</th> <th></th> <th></th> <th></th> <th></th>				<u></u> З	GND		GND				
5-52 4 101 16 18			B 2 9						B 2 9		
$ \begin{array}{cccccccccccccccccccccccccccccccccccc$			B 3 0	4	PCIe RX		PCIe RX		B30		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $			В 3 2	4					B32		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $				5							
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $			B 3 5	~	GND		GND		B35		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $			В37						B37		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $				7		DIFF PAIR		7			
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $			B 4 0	7	PCIe RX		PCIe RX	7	B40		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $						DISCRETE					
244 5 92 (1 × 3X D17 P A(12 PC (1 × 3X 0 324 246 6 YC D11 YA12 PC (1 × 3X 0 324 246 7 YC N D11 YA12 PC (1 × 3X 0 324 247 8 YC 8X D17 P A(12 PC (1 × 3X 0 244 9 248 9 PC (1 × 3X D11 P A(12 PC (1 × 3X 9 245 248 9 PC (1 × 3X D17 P A(12 PC (1 × 3X 9 245 253 10 PC (1 × 3X D17 P A(12 PC (1 × 3X 10 255 251 10 PC (1 × 3X D17 P A(12 PC (1 × 3X 11 254 254 1 PC (1 × 3X D17 P A(12 PC (1 × 3X 11 254 254 1 PC (1 × 3X D17 P A(12 PC (1 × 3X 11 254 254 1 PC (1 × 3X D17 P A(12 PC (1 × 3X 11 254 255 12 PC (1 × 3X D17 P A(12 PC (1 × 3X 11 254 255 12 PC (1 × 3X D17 P A(12 PC (1 × 3X 12 255 256			R 4 3						B 4 3		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $			B 4 4		PCIe RX		PCIe RX		B44		
b22 3 PC1E KX D1F PATR PC1E KX 9 548 b53 GND GND GND GND 549 c551 13 PC1a KX D1FF PATR P21e KX 12 551 b12 GND GND GND 531 b22 GND GND GND 533 c53 11 PC1e KX D1FF PATR P21e KX 17 533 c54 1 PC1e KX D1FF PATR P21e KX 17 533 c55 12 PC1e KX D1FF PATR P21e KX 17 535 c55 12 PC1e KX D1FF PATR P21e KX 17 535 c55 12 PC1e KX D1FF PATR P21e KX 17 535 c55 12 PC1e KX D1FF PATR P21e KX 16 536 c56 13 PC1e KX D1FF PATR P21e KX 13 566 c56 14 PC1e KX D1FF PATR P21e KX 14 563 c56 15 PC1e KX D1FF PATR P21e KX 14 563 c56 15 PC1e KX D1FF PATR P21e KX 14				8		DIFF PAIR		8			
548 6R2 6R2 6R2 849 550 10 PC1c 42 D111 PALR FC1c RX 10 851 552 6R5 6R2 6R2 851 553 11 PC1c 4X D111 PALR FC1c RX 11 852 553 11 PC1c 4X D111 PALR FC1c RX 11 853 554 12 PC1c 4X D111 PALR FC1c RX 12 855 555 13 PC1c 4X D117 PALR FC1c RX 13 859 555 13 PC1c 4X D177 PALR FC1c RX 13 859 555 13 PC1c 4X D177 PALR FC1c RX 13 859 555 13 PC1c 4X D177 PALR FC1c RX 13 859 555 13 PC1c 4X D177 PALR FC1c RX 14 862 754 GN2 GND GND B61 863 755 15 PC1c 4X D177 PALR FC1c RX 5 85 755 15 PC1c 4X D177 PALR FC1c RX 5 86 755 15 PC1c 4X D177 PALR FC1c RX 5 <											
bal 10 PCIe RX D FF PA R PCIe RX 10 901 b52 GND GND GND GND 553 b52 11 PCIe RX DIFF PAIR PCIe RX 11 553 b54 11 PCIe RX DIFF PAIR PCIe RX 11 554 b55 CND GND GND 554 554 11 PCIe RX 11 554 b56 CND GND GND 555 556 566 567 757 741R 751e RX 113 569 581 GC10 RX D1FF FA1R PC1e RX D1FF FA1R 761e RX 14 362 563 15 PC1e RX D1FF FA1R PC1e RX D1F 14 362 563			B49		GND		GND		B49		
352 CND CND B52 353 PCIC RX DTF PAIR PCIC RX 11 B53 354 TT PCI RX DTF PAIR PCIC RX 11 B54 355 GND GND GND B55 358 12 PCI RX DTF PAIR PCI RX 12 B56 357 12 PCI RX DTF PAIR PCI RX 12 B57 358 CND CND CND B58 359 S PCI RX DTF PAIR PCI RX 12 B57 358 CND CND B58 B59 359 TS PCI RX DTF PAIR PCI RX 13 B59 363 T4 PCI RX DTF PAIR PCI RX 14 B62 363 T4 PCI RX DTF PAIR PCI RX 14 B62 363 T4 PCI RX DTF PAIR PCI RX 15 B63 363 T5 PCI RX DTF PAIR PCI RX 15 B63 364 CND CND B44 B63 B64 B64 365 T5 PCI RX DTF PAIR PCI RX T											
354 11 PCIe RX DIFF PAIR PCIe RX 17 354 355 GND GND GND GND 355 155 12 FCIe RX DIFF PAIR PCIe RX 12 355 357 12 PCIe RX DIFF PAIR PCIe RX 12 357 358 GND GND 358 359 13 FCIe RX DIFF PAIR PCIe RX 13 350 260 13 FCIe RX DIFF PAIR PCIe RX 13 350 861 GND GND B81 362 14 PCIe RX DIFF PAIR PCIe RX 14 352 263 14 PCIe RX DIFF PAIR PCIe RX 14 353 364 GND CND 384 365 15 PCIe RX DIFF PAIR PCIe RX 15 355 365 15 PCIe RX DIFF PAIR PCIe RX 15 356 365 15 PCIe RX DIFF PAIR PCIe RX 15			B 5 2	1 1	GND		GND	1 1	B52		
056 12 PCIe RX DIFF PAIR PCIe RX 12 D56 657 12 PCIe RX DIFF PAIR PCIe RX 12 B57 B58 GND GND DF8 B56 13 PCIe RX DIFF PAIR PCIe RX 13 B59 B60 13 PCIe RX DIFF PAIR PCIe RX 13 B60 B61 CND GND B1 B2 B62 14 PCIe RX DIFF PAIR PCIe RX 14 B62 B63 14 PCIe RX DIFF PAIR PCIe RX 14 B63 B64 GND GND B64 GND B64 B65 15 PCIe RX DIFF PAIR PCIe RX 15 B56 B66 15 PCIe RX DIFF PAIR PCIe RX 15 B56 B67 GND GND B67 GND B67 GND B68 B67 GND B15 B56 B68 B68 B68 B68 B68 B68 B6			B 5 4		PCIe RX		PCIe RX		B54		
557 12 PCIe RX DIFF PAIR PCIe RX 12 557 558 GND GND GND S58 658 13 PCIe RX DIFF PAIR PCIe RX 13 558 660 13 PCIe RX DIFF PAIR PCIe RX 13 560 661 CNB CNB S61 S61 S61 S61 662 14 PCIe RX DIFF PAIR PCIe RX 14 562 663 14 PCIe RX DIFF PAIR PCIe RX 14 563 664 CND CND S64 S64 S64 565 15 PCIe RX DIFF PAIR PCIe RX 15 565 566 15 PCIe RX DIFF PAIR PCIe RX 15 566 566 15 PCIe RX DIFF PAIR PCIe RX 15 566 567 GND SCRETF RESERVED 568 566 566 568 RESERVED DISCRETF RESERVED 568 568				1 2		DIFF PAIR		1 2			
B59 13 PCIe RX DIFF PAIR PCIe RX 13 B59 B60 13 PCIe RX DIFF PAIR PCIe RX 13 B60 B61 GND GND GND B61 B62 14 PCIe RX DIFF PAIR PCIe RX 14 B62 B63 14 PCIe RX DIFF PAIR PCIe RX 14 B63 B64 CND GND B64 B65 15 PCIe RX DIFF PAIR PCIe RX 15 B65 B66 15 PCIe RX DIFF PAIR PCIe RX 15 B66 B66 15 PCIe RX DIFF PAIR PCIe RX 15 B66 B66 15 PCIe RX DIFF PAIR PCIe RX 15 B66 B66 CND GND GND B67 GND GND GND B68 RESERVED DISCRETE RESERVED B69 GND GND GND B69 RESERVED DISCRETE RESERVED B69 GND GND			B 5 7		PCIe RX		PCIe RX		B 5 7		
B61GNDGNDB61B6214PCIe RXDIFF PAIRPCIe RX14B62B6314PCIe RXDIFF PAIRPCIe RX14B63B64GNDGNDB64B6515PCIe RXDIFF PAIRPCIe RX15B65B6615PCIe RXDIFF PAIRPCIe RX15B66B67GNDB67GNDB67B68B67B68B68RESERVEDDISCRETERESERVEDB68B69B69B69B70PRSNT DETECTDISCRETEPCSNT DETECTB70MaximAnimMaximActivityPRSNT DETECTDISCRETEPRSNT DETECTB70			B 5 9		PCIe RX		PCIe RX		B59		
B6214PCIe RXDIFF PAIRPCIe RX14B62B6314PCIe RXDIFF PAIRPCIe RX14B63B64GNDGNDB64B6515PCIe RXDIFF PAIRPCIe RX15B65B6615PCIe RXDIFF PAIRPCIe RX15B66B67GNDGNDB67B67B67B67B68RESIRVEDDISCRETERESERVEDB68B69RESIRVEDB68B69RESIRVEDDISCRETERESERVEDB69B69B70PRSNT DETECTDISCRETEPRSNT DETECTB70				13		DIFF PAIR		13			
B64GNDGNDB64B6515PCIe RXDIFF PAIRPCIe RX15B65B6615PCIe RXDIFF PAIRPCIe RX15B66B67GNDGNDB67B67B68RESERVEDDISCRETERESERVEDB68B69RESERVEDDISCRETERESERVEDB69B70PRSNT DETECTDISCRETEPRSNT DETECTB70			B 6 2		PCIe RX		PCIe RX		B62		
B6615PCIeRXDIFFPAIRPCIeRX15B66B67GNDGNDB67B68RESERVEDDISCRETERESERVEDB68B69RESERVEDDISCRETERESERVEDB69B70PRSNT DETECTDISCRETEPRSNT DETECTB70				4		UIFF PAIR					
B67GNDGNDB67B68RESERVEDDISCRETERESERVEDB68B69RESERVEDDISCRETERESERVEDB69B70PRSNT DETECTDISCRETEPRSNT DETECTB70											
DOORESERVEDDISCRETERESERVEDDOOB69RESERVEDDISCRETERESERVEDB69B70PRSNT DETECTDISCRETEPRSNT DETECTB70			B 6 7		GND		GND		B67	0 PL	THERWISE SPECIFIED: APVD - C ±- PRODUCT SPEC
B70 PRSNT DETECT DISCRETE PRSNT DETECT B70 - HADLES - HAD										2 PL 3 PL 4 PL	C ±0.13 - C ±0.013 APPLICATION SP C ±0.0001
										MATERIAL FINIS	Lj

4805 (3/13)

		REVISIONS			
Ρ	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-

1

(
1	/	

А

DWN 06AUG2018 <u>В. MATTHEWS</u> снк 06AUG2018 <u>G. GUTGOLD</u> IS A CONTROLLED DOCUMENT. TE Connectivity TOLERANCES UNLESS OTHERWISE SPECIFIED: AME SLIVER 2.0 4C 30AWG RIGHT ANGLE TO RIGHT ANGLE 0 PLC ±-1 PLC ±0.5 2 PLC ±0.13 3 PLC ±0.013 4 PLC ±0.0001 ANGLES +-RODUCT SPEC APPLICATION SPEC size cage code drawing no A 1 0 0 7 7 9 C - 2 3 4 0 8 8 5 RESTRICTED TO INISH IGH' -SCALE 1:1 SHEET OF 3 REV A USTOMER DRAWING -