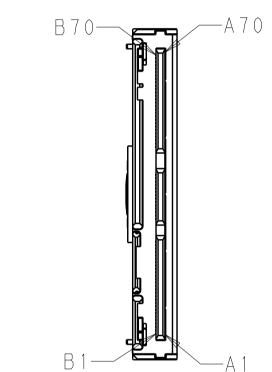


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	REVISIONS				
LTR	DESCRIPTION		DATE	DWN	APVD
1	PRELIMINARY RELEASE		06AUG2018	ВM	GG
-	LTR 1	LTR DESCRIPTION	LTR DESCRIPTION	LTR DESCRIPTION DATE	LTR DESCRIPTION DATE DWN



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	500 +10/-0	2340882-1
	"L" MM	TE P/N
A CONTROLLED DOCUMENT. DWN 06AUG20 B. MATTHEWS CHK 06AUG20 G. GUTGOLD		TE Connectivity
TOLERANCES UNLESS OTHERWISE SPECIFIED: O. D. OUTOULD 0 PLC - 1 PLC ±0.5 2 PLC ±0.13 3 PLC ±0.013	- SLIVER 2.0 4C 30A STRAIGHT TO STRAI -	
4 PLC ±0.0001 ANGLES ± FINISH WEIGHT	size cage code drawing no A 1 0 0 7 7 9 C - 2 3 4 0 8 8 2	RESTRICTED TO
CUSTOMER DRAWING	A 0011962340002 scale	

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4805 (3/13)

PIN NO.	P1 PAIR NO.	DESIGNATION	WIRE TYPE	DESIGNATION	P2 PAIR NO.	PIN NO
A 1		RETURN	DISCRETE	12V POWER		A 1
A 2		RETURN	DISCRETE	12V POWER		A 2
A 3		RETURN	DISCRETE	12V POWER		A 3
A 4		RETURN	DISCRETE	12V POWER		A 4
A 5		RETURN	DISCRETE	12V POWER		A 5
A 6		RETURN	DISCRETE	12V POWER		A 6
Α7		SIDE BAND	DISCRETE	SIDE BAND		Α7
A 8		SIDE BAND	DISCRETE	SIDE BAND		A 8
A 9		SIDE BAND	DISCRETE	SIDE BAND		A 9
A 1 0		SIDE BAND	DISCRETE	SIDE BAND		A 1 0
A 1 1		SIDE BAND	DISCRETE	SIDE BAND		A 1 1
A 1 2		SIDE BAND	DISCRETE	SIDE BAND		A 1 2
A 1 3		GND		GND		A 1 3
A 1 4		CLK	DIFF PAIR	CLK		A 1 4
A 1 5		CLK	DIFF PAIR	CLK		A 1 5
A16		GND		GND		A 1 6
A 1 7	0	РСІеТХ	DIFF PAIR	РСІеТХ	0	A 1 7
A 1 8	0	PCIe TX	DIFF PAIR	PCIe TX	0	A 1 8
A 1 9		GND		GND		A 1 9
	1				1	
A 2 0		РСІеТХ	DIFF PAIR	РСІеТХ		A 2 0
A 2 1	1	PCIe TX	DIFF PAIR	PCIe TX	1	A 2 1
A 2 2		GND		GND		A 2 2
A 2 3	2	РСІеТХ	DIFF PAIR	РСІеТХ	2	A 2 3
A 2 4	2	PCIe TX	DIFF PAIR	PCIe TX	2	A 2 4
A 2 5		GND		GND		A 2 5
	<u>γ</u>				\sim	
A 2 6	3	PCIeTX	DIFF PAIR	PCIeTX	3	A 2 6
A 2 7	3	PCIe TX	DIFF PAIR	PCIe TX	3	A 2 7
A 2 8		GND		GND		A 2 8
		KEY		KEY		
A 2 9		GND		GND		A 2 9
A 3 0	4	PCIeTX	DIFF PAIR	РСІеТХ	4	A 3 0
A 3 1	4	PCIe TX	DIFF PAIR	PCIe TX	4	A 3 1
A 3 2		GND		GND		A 3 2
A 3 3	5	РСІеТХ	DIFF PAIR	РСІеТХ	5	A 3 3
A 3 4	5	PCIe TX	DIFF PAIR	PCIe TX	5	A 3 4
A 3 5		GND		GND		A 3 5
A 3 6	6	РСІеТХ	DIFF PAIR	РСІеТХ	6	A 3 6
A 3 7	6	PCIe TX	DIFF PAIR	PCIe TX	6	A 3 7
A 3 8	✓	GND		GND		A 3 8
A 3 9	/	РСІеТХ	DIFF PAIR	РСІеТХ	/	A 3 9
A40	7	PCIe TX	DIFF PAIR	PCIe TX	7	A 4 0
A 4 1		GND		GND		A 4 1
A 4 2		SIDE BAND	DISCRETE	SIDE BAND		A 4 2
		KEY		KEY		
A 4 3		GND		GND		A 4 3
	0					
A 4 4	8	PCIeTX	DIFF PAIR	PCIeTX	8	A 4 4
A 4 5	8	PCIe TX	DIFF PAIR	PCIe TX	8	A 4 5
A46		GND		GND		A 4 6
A 4 7	9	РСІеТХ	DIFF PAIR	РСІеТХ	9	A 4 7
A 4 8	9	PCIe TX	DIFF PAIR	PCIe TX	9	A 4 8
A 4 9		GND		GND		A 4 9
A 5 0	10	PCIeTX	DIFF PAIR	РСІеТХ	1 0	A 5 0
A 5 1	10	PCIe TX	DIFF PAIR	PCIe TX	1 0	A 5 1
A 5 2		GND		GND	A	A 5 2
A 5 3	1 1	РСІеТХ	DIFF PAIR	РСІеТХ	1 1	A 5 3
A 5 4	1 1	PCIe TX	DIFF PAIR	PCIe TX	1 1	A 5 4
A55		GND		GND		A 5 5
A56	12	PCIeTX	DIFF PAIR	РСІеТХ	1 2	A 5 6
A 5 7	1 2	PCIe TX	DIFF PAIR	PCIe TX	1 2	A 5 7
A 5 8		GND		GND		A 5 8
	1 2				1 ~	
A 5 9	13	PCIeTX	DIFF PAIR	PCIeTX	13	A 5 9
A 6 0	13	PCIe TX	DIFF PAIR	PCIe TX	1 3	A 6 0
A 6 1		GND		GND		A 6 1
A 6 2	1 4	PCIeTX	DIFF PAIR	PCIeTX	1 4	A 6 2
A 6 3	14	PCIe TX		PCIe TX	1 4	A 6 3
	4		DIFF PAIR		4	
A 6 4		GND		GND		A 6 4
A65	15	РСІеТХ	DIFF PAIR	РСІеТХ	1 5	A 6 5
A66	15	PCIe TX	DIFF PAIR	PCIe TX	1 5	A66
A 6 7		GND		GND		A 6 7
🗸 1		RESERVED	DISCRETE	RESERVED		A 6 8
ΔΑΧ					1	I AVO
A 6 8 A 6 9		RESERVED	DISCRETE	RESERVED		A 6 9

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THIS DRAWING IS A (DIMENSIONS: mm MATERIAL --

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	-	SEE SHEET 1	-	-	-

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\ C	ONTROLLED DOCUMENT.	DWN 06AUG2018 В. MATTHEWS снк 06AUG2018 G. GUTGOLD	=1	E TE	Connec	tivity	
	TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC \pm - 1 PLC \pm 0.5 2 PLC \pm 0.13 3 PLC \pm 0.013	APVD	NAME SLIVER 2.0 4C STRAIGHT TO S -				
	4 PLC ±0.0001 ANGLES ±- FINISH	- WEIGHT _	size cage code drawing no $A \downarrow 0 0 7 7 9 \bigcirc 23$			R	ESTRICTED TC
	-	CUSTOMER DRAWING		scale 1:1	SHEET 2	OF 3	REV 1

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4805 (3/13)

PIN NO.	P' PAIR NO.	DESIGNATION	WIRE TYPE	F DESIGNATION	P2 PAIR NO.	PIN NO
B 1		12V POWER	DISCRETE	12V POWER		B 1
B 2		12V POWER	DISCRETE	12V POWER		B 2
B 3		12V POWER		12V POWER		B3
			DISCRETE			
B 4		12V POWER	DISCRETE	12V POWER		B 4
B 5		12V POWER	DISCRETE	12V POWER		В5
B6		12V POWER	DISCRETE	12V POWER		B 6
B 7		SIDE BAND	DISCRETE	SIDE BAND		B 7
B8		SIDE BAND	DISCRETE	SIDE BAND		B8
В9		SIDE BAND	DISCRETE	SIDE BAND		В9
B10		SIDE BAND	DISCRETE	SIDE BAND		B10
B 1 1		3.3 AUX POWER	DISCRETE	3.3 AUX POWER		B 1 1
B12		SIDE BAND	DISCRETE	SIDE BAND		B12
B13		GND		GND		B13
B14		CLK	DIFF PAIR	CLK		B14
B15		CLK	DIFF PAIR	CLK		B15
B16		GND		GND		B16
B17	0	PCIe RX	DIFF PAIR	PCIe RX	0	B 1 7
B18	0	PCIe RX	DIFF PAIR	PCIe RX	0	B18
B19		GND		GND		B19
	1				1	
B20		PCIe RX	DIFF PAIR	PCIe RX		B20
B 2 1	1	PCIe RX	DIFF PAIR	PCIe RX	1	B 2 1
B22		GND		GND		B 2 2
B 2 3	2	PCIe RX	DIFF PAIR	PCIe RX	2	B 2 3
B 2 4	2	PCIe RX	DIFF PAIR	PCIe RX	2	B 2 4
	Ĺ		UTIL LATE		L	
B 2 5		GND		GND		B 2 5
B26	3	PCIe RX	DIFF PAIR	PCIe RX	3	B26
B27	3	PCIe RX	DIFF PAIR	PCIe RX	3	B 2 7
B28		GND		GND		B28
		KEY		KEY		
B 2 9		GND		GND		B29
B30	4	PCIe RX	DIFF PAIR	PCIe RX	4	B30
B 3 1	4	PCIe RX	DIFF PAIR	PCIe RX	4	B 3 1
B32		GND		GND		B 3 2
B33	5	PCIe RX	DIFF PAIR	PCIe RX	5	B33
					_	
B 3 4	5	PCIe RX	DIFF PAIR	PCIe RX	5	B 3 4
B35		GND		GND		B35
B36	6	PCIe RX	DIFF PAIR	PCIe RX	6	B36
B37	6	PCIe RX	DIFF PAIR	PCIe RX	6	B 3 7
B 3 8	~	GND		GND		B38
B39	/	PCIe RX	DIFF PAIR	PCIe RX	7	B39
B40	7	PCIe RX	DIFF PAIR	PCIe RX	7	B40
B41		GND		GND		B 4 1
B 4 2		PRSNT DETECT	DISCRETE	PRSNT DETECT		B 4 2
D 4 Z			DISCRETE			
		K E Y		KEY		
B43		GND		GND		B 4 3
B44	8	PCIe RX	DIFF PAIR	PCIe RX	8	B 4 4
B 4 5	8	PCIe RX	DIFF PAIR	PCIe RX	8	B 4 5
					+ ~	
B 4 6		GND		GND		B 4 6
B 4 7	9	PCIe RX	DIFF PAIR	PCIe RX	9	B 4 7
B48	9	PCIe RX	DIFF PAIR	PCIe RX	9	B48
B49		GND		GND		B49
B 5 0	1 0	PCIe RX	DIFF PAIR	PCIe RX	1 0	B 5 0
B 5 1	10	PCIe RX	DIFF PAIR	PCIe RX	10	B 5 1
B52		GND		GND		B52
B53	1 1	PCIe RX	DIFF PAIR	PCIe RX	1 1	B53
B54	1 1	PCIe RX	DIFF PAIR	PCIe RX	1 1	B 5 4
B 5 5		GND		GND	+	B54 B55
	1 \				1 0	
B 5 6	12	PCIe RX	DIFF PAIR	PCIe RX	12	B 5 6
B57	12	PCIe RX	DIFF PAIR	PCIe RX	1 2	B 5 7
B58		GND		GND		B58
B 5 9	1 3	PCIe RX	DIFF PAIR	PCIe RX	1 3	B 5 9
	13				13	
B60	13	PCIe RX	DIFF PAIR	PCIe RX	+ 13	B60
B 6 1		GND		GND	<u> </u>	B 6 1
B62	14	PCIe RX	DIFF PAIR	PCIe RX	1 4	B 6 2
B63	14	PCIe RX	DIFF PAIR	PCIe RX	1 4	B63
B 6 4		GND		GND	+ • •	B 6 4
	1 5				1 E	
B65	15	PCIe RX	DIFF PAIR	PCIe RX	15	B65
	15	PCIe RX	DIFF PAIR	PCIe RX	15	B66
B66						B67
B66 B67		GND		GND		
B67			DISCRETE			
		GND RESERVED RESERVED	DISCRETE DISCRETE	RESERVED		B68 B69

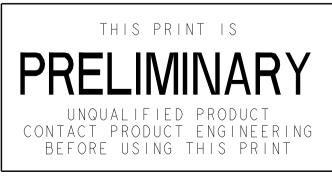
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DWN 06AUG2018 B. MATTHEWS снк 06AUG2018 G. GUTGOLD - М THIS DRAWING IS A CONTROLLED DOCUMENT. TE Connectivity TOLERANCES UNLESS OTHERWISE SPECIFIED: DIMENSIONS: SLIVER 2.0 4C 30AWG STRAIGHT TO STRAIGHT mm 0 PLC ±-1 PLC ±0.5 2 PLC ±0.13 3 PLC ±0.013 4 PLC ±0.0001 ANGLES +-PRODUCT SPEC APPLICATION SPEC size cage code drawing no A 1 0 0 7 7 9 \bigcirc 2 3 4 0 8 8 2 RESTRICTED TO MATERIAL FINISH EIGHT --SCALE 1:1 SHEET OF 3 REV 1 CUSTOMER DRAWING -

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			REVISIONS			
Ρ	LTR		DESCRIPTION	DATE	DWN	APV
	-	SEE SHEET 1		-	-	-



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