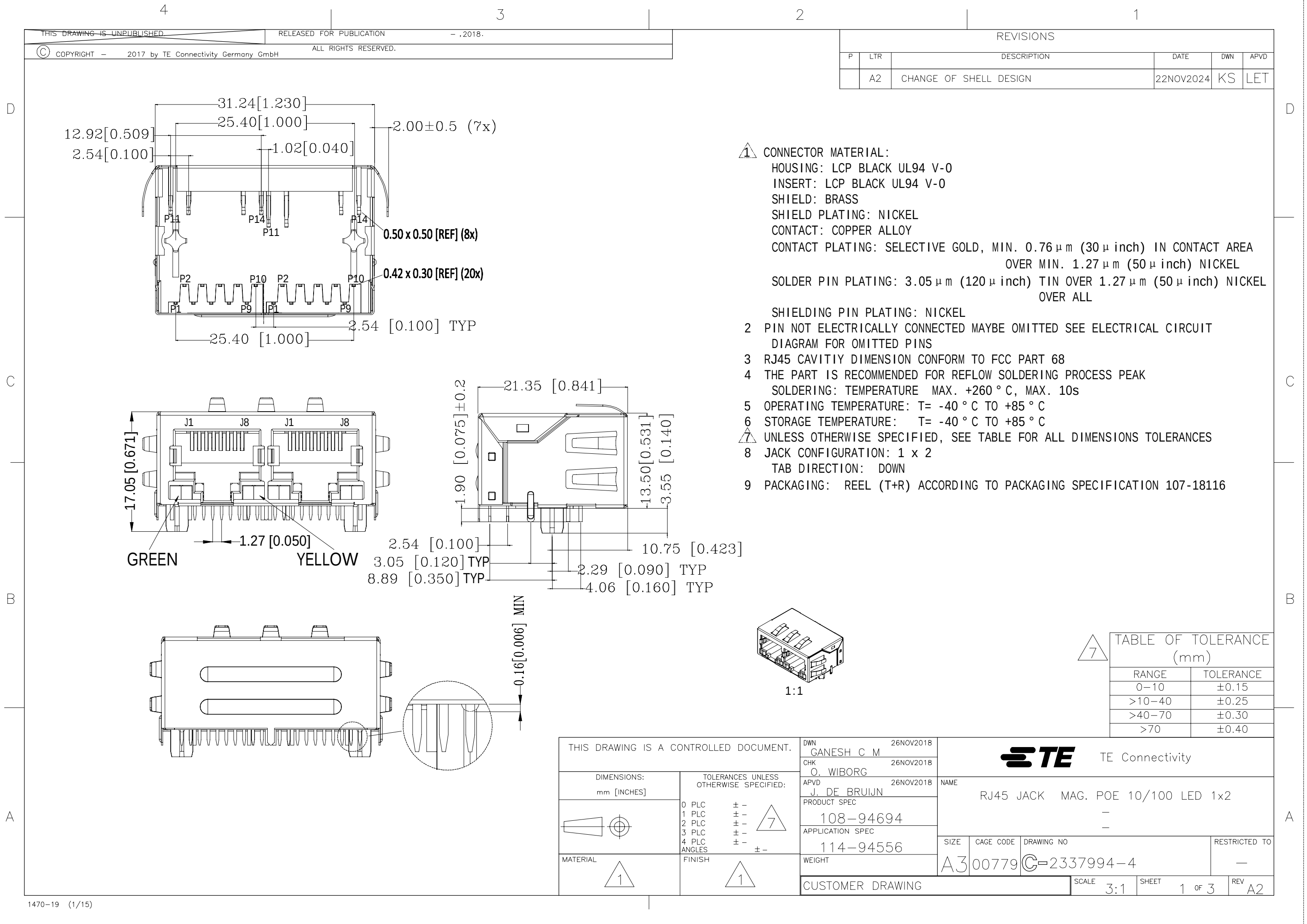
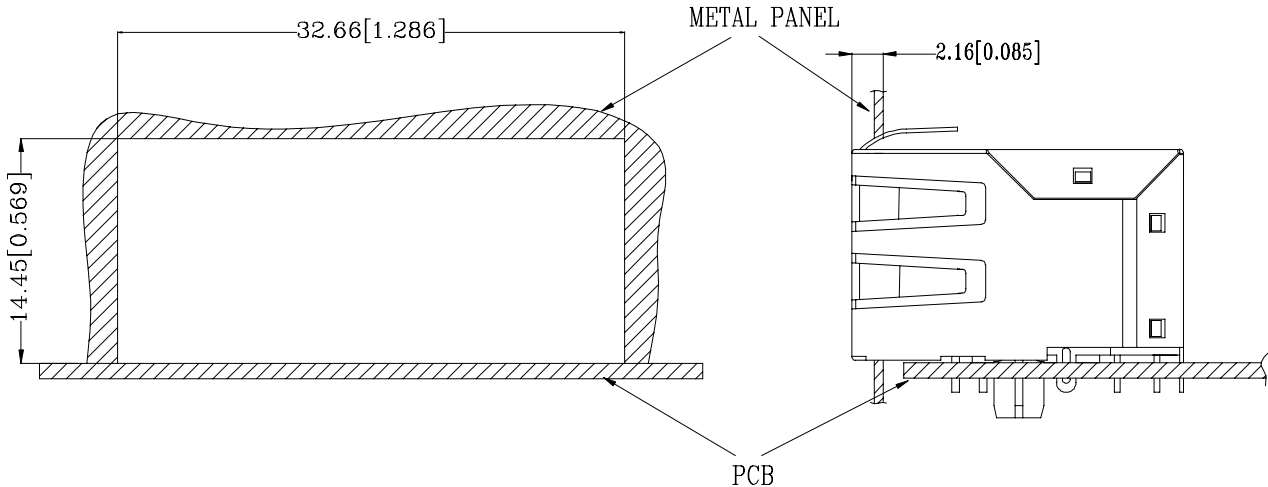
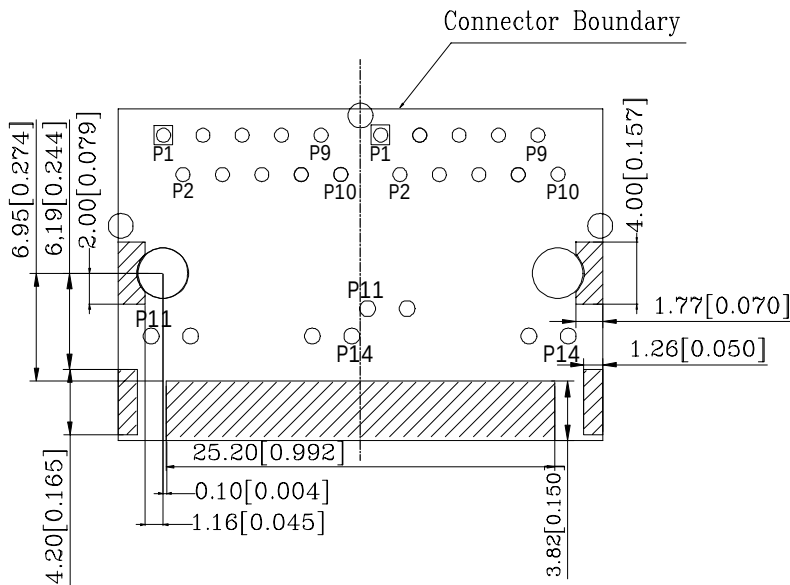
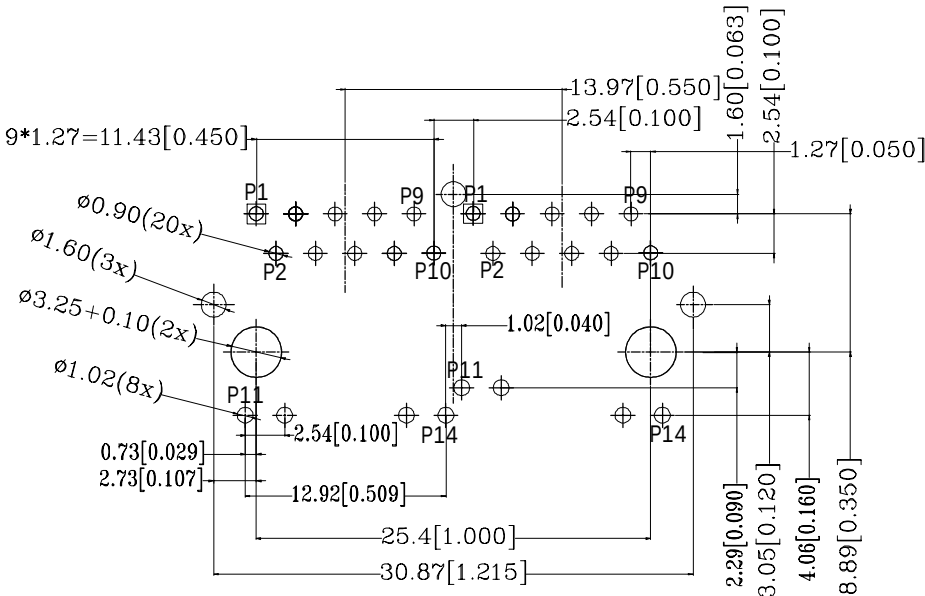




REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	—	SEE SHEET 1	—	—	—

SUGGESTED PCB LAYOUT



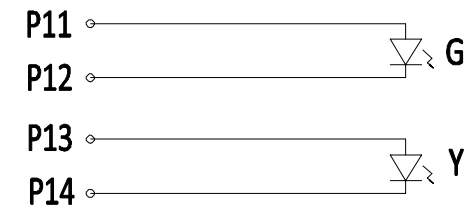
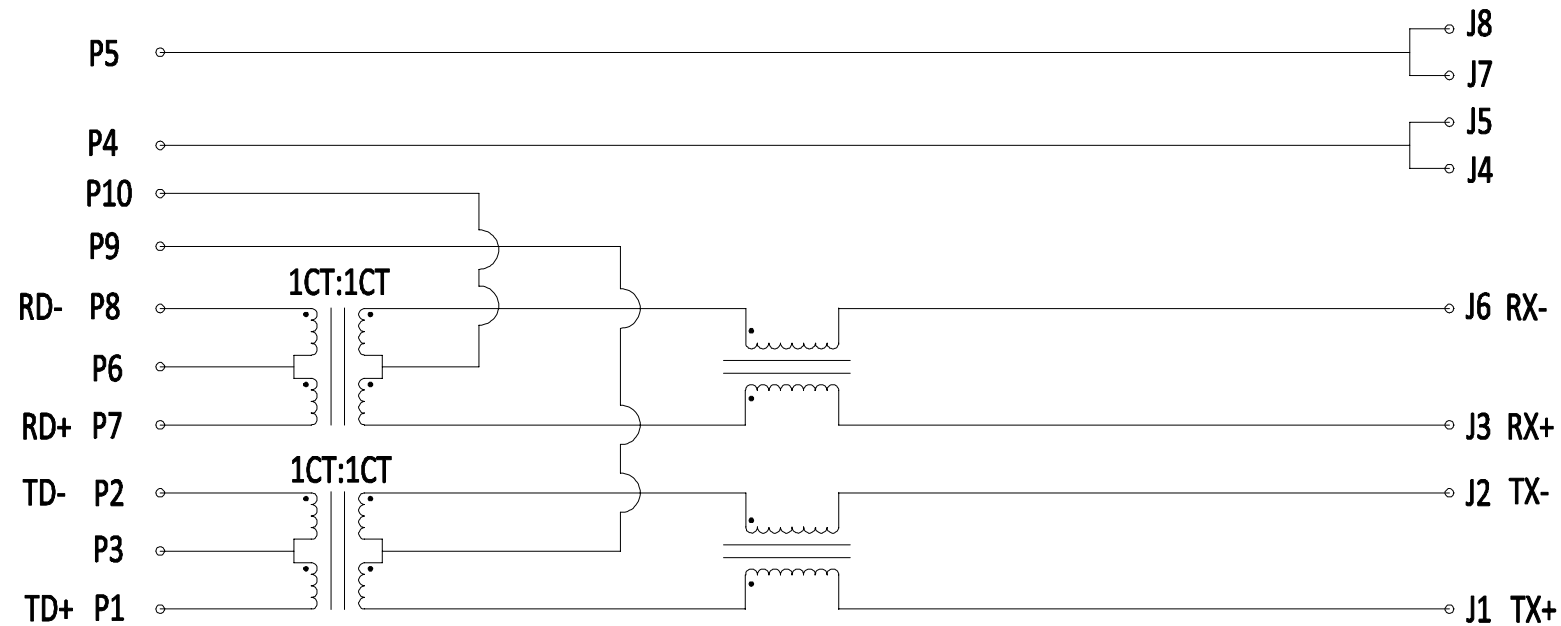
UNIT: mm / inch
TOLERANCES: ±0.10 / 0.004
ONLY FOR THIS VIEW

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN	26NOV2018	TE TE Connectivity		
		CHK	26NOV2018			
		APVD	26NOV2018	NAME		
		PRODUCT SPEC		RJ45 JACK MAG. POE 10/100 LED 1x2		
		APPLICATION SPEC				
		WEIGHT		SIZE	CAGE CODE	DRAWING NO
				A3	00779	C=2337994-4
		CUSTOMER DRAWING		SCALE	3:1	SHEET
						2 of 3
						REV
						A2

REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-

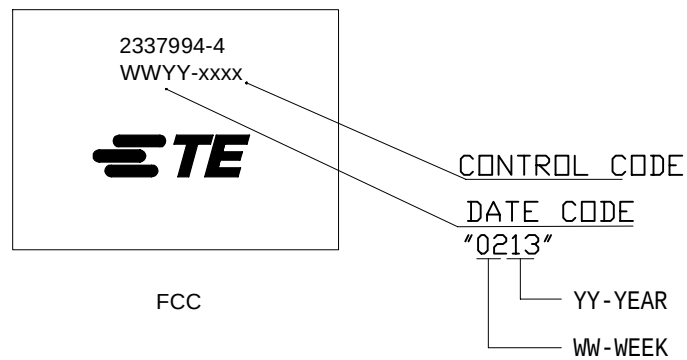
ELECTRICAL CIRCUIT DIAGRAM

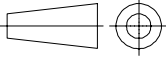


Pin	Green	Pin	Yellow
P11	+	P13	+
P12	-	P14	-

WIRE BONDED , V_f= 1.8V ~ 2.4V, I_f= 20mA

- TURN RATIO @100kHz: (P1~P2):(J1~J2) = 1:1±2%
(P7~P8):(J3~J6) = 1:1±2%
- PRIMARY INDUCTANCE: 350µH MIN @100kHz,0.1V 8mA DC BIAS
- DC RESISTANCE: 1.2 OHMS MAX
- INSERTION LOSS: 1-100MHz -1.2dB MAX
- RETURN LOSS: 1-30MHz -16dB MIN. (@30MHz -17.0dB TYP)
30-60MHz -16dB+20log(f/30MHz) MIN.
(@60MHz -13.0dB TYP)
60-80MHz -10dB MIN.(@80MHz -11.0dB TYP)
- CROSS TALK: 1-100MHz -30dB MIN.
- COMMON TO COMMON MODE ATTENUATION: 1-100MHz -30dB MIN.
- ISOLATION: PHY SIDE TO LINE SIDE: 500VAC
- DC CURRENT/VOLTAGE RATING-PSE PINS: 720mA @57V(CONTINUOUS)



THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN GANESH C M 26NOV2018		 TE Connectivity													
		CHK O. WIBORG 26NOV2018															
DIMENSIONS: mm [INCHES]		TOLERANCES UNLESS OTHERWISE SPECIFIED:		NAME RJ45 JACK MAG. POE 10/100 LED 1x2													
		0 PLC ± - 1 PLC ± - 2 PLC ± - 3 PLC ± - 4 PLC ± - ANGLES ± - 															
MATERIAL 		FINISH 		APVD J. DE BRUIJN 26NOV2018		SIZE A3		CAGE CODE 00779		DRAWING NO C=2337994-4		RESTRICTED TO —					
				PRODUCT SPEC 108-94694													
				APPLICATION SPEC 114-94556													
				WEIGHT													
CUSTOMER DRAWING												SCALE 3:1		SHEET 3 OF 3		REV A2	