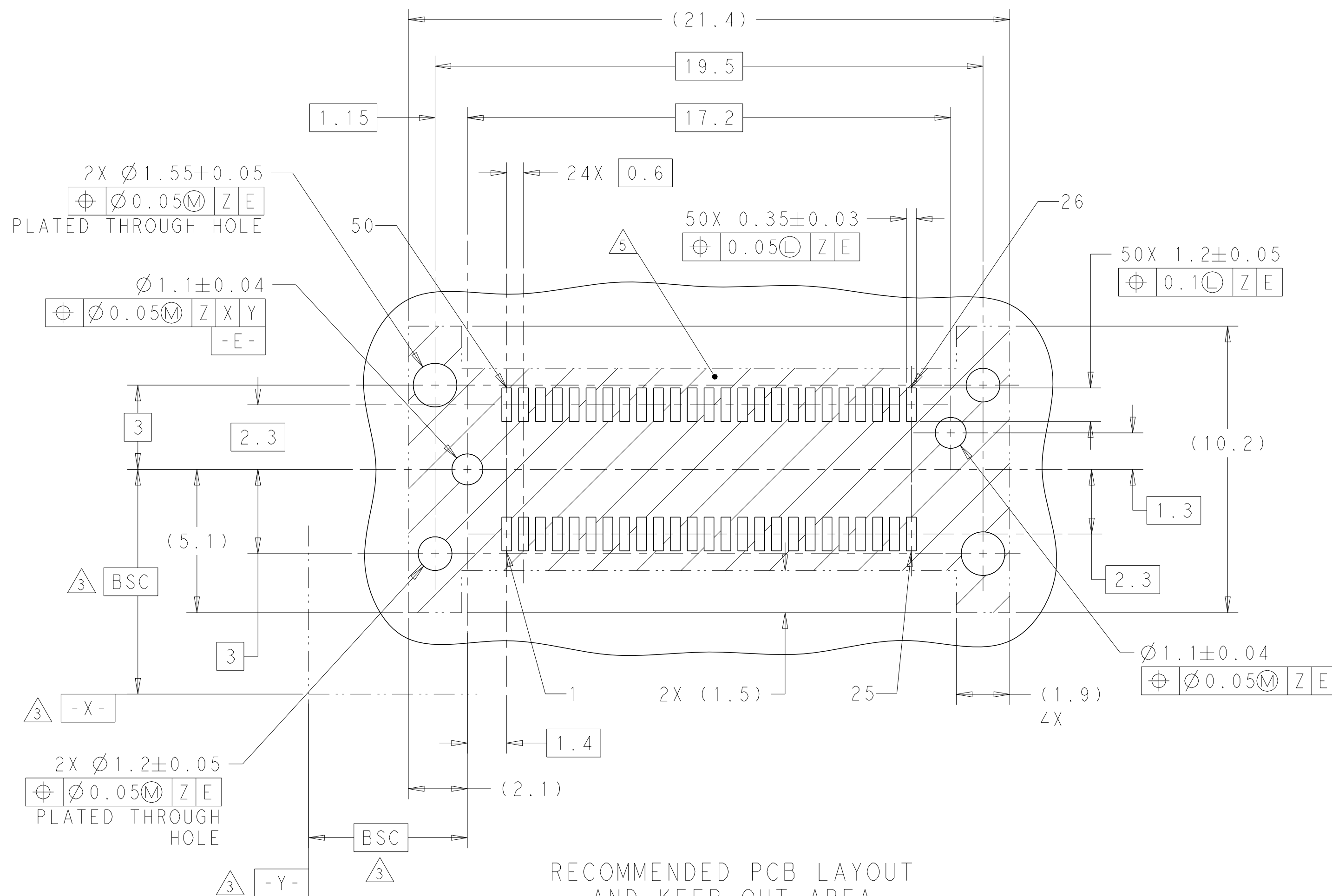


P	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-



RECOMMENDED PCB LAYOUT
AND KEEP OUT AREA
SCALE 8:1

PIN #	DESCRIPTION	PIN #	DESCRIPTION
1	SIGNAL GROUND	50	SIGNAL GROUND
2	SIGNAL	49	SIGNAL
3	SIGNAL	48	SIGNAL
4	SIGNAL GROUND	47	SIGNAL GROUND
5	SIGNAL	46	SIGNAL
6	SIGNAL	45	SIGNAL
7	SIGNAL GROUND	44	SIGNAL GROUND
8	SIGNAL	43	SIGNAL
9	SIGNAL	42	SIGNAL
10	SIGNAL GROUND	41	SIGNAL GROUND
11	SIGNAL	40	SIGNAL
12	SIGNAL	39	SIGNAL
13	SIGNAL GROUND	38	SIGNAL GROUND
14	SIGNAL	37	SIGNAL
15	SIGNAL	36	SIGNAL
16	SIGNAL GROUND	35	SIGNAL GROUND
17	SIGNAL	34	SIGNAL
18	SIGNAL	33	SIGNAL
19	SIGNAL GROUND	32	SIGNAL GROUND
20	SIGNAL	31	SIGNAL
21	SIGNAL	30	SIGNAL
22	SIGNAL GROUND	29	SIGNAL GROUND
23	SIGNAL	28	SIGNAL
24	SIGNAL	27	SIGNAL
25	SIGNAL GROUND	26	SIGNAL GROUND