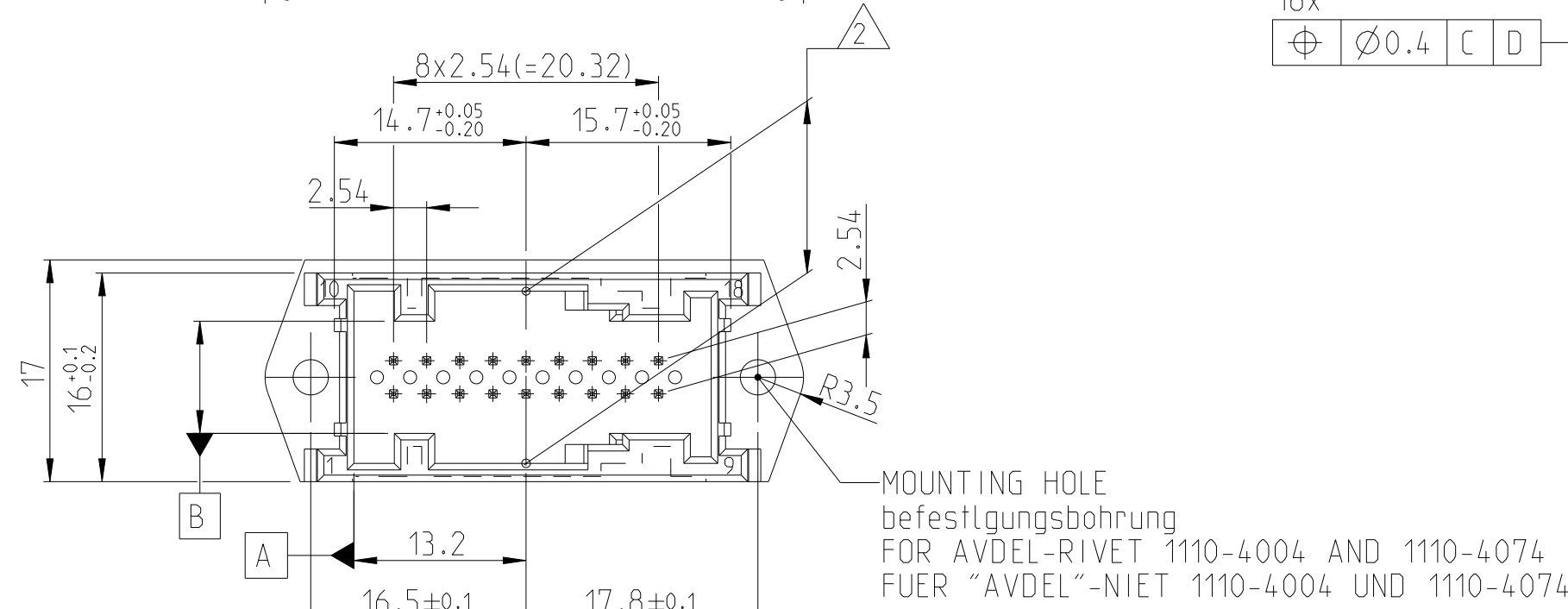
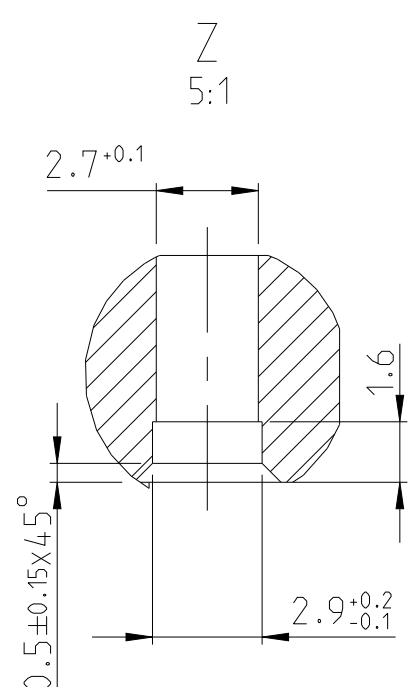
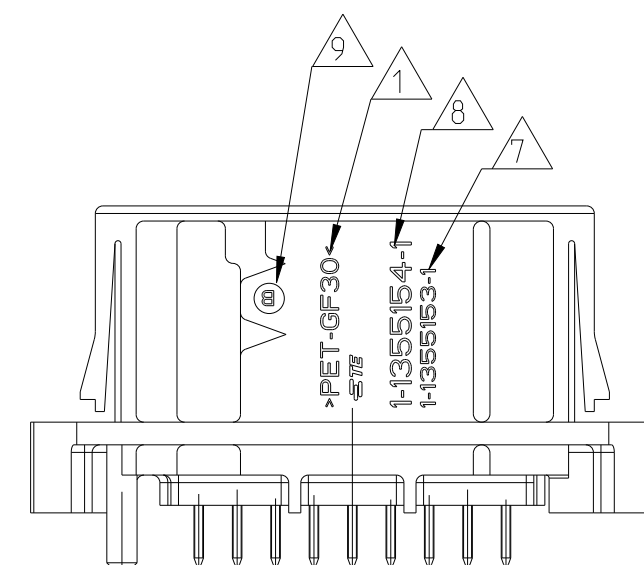
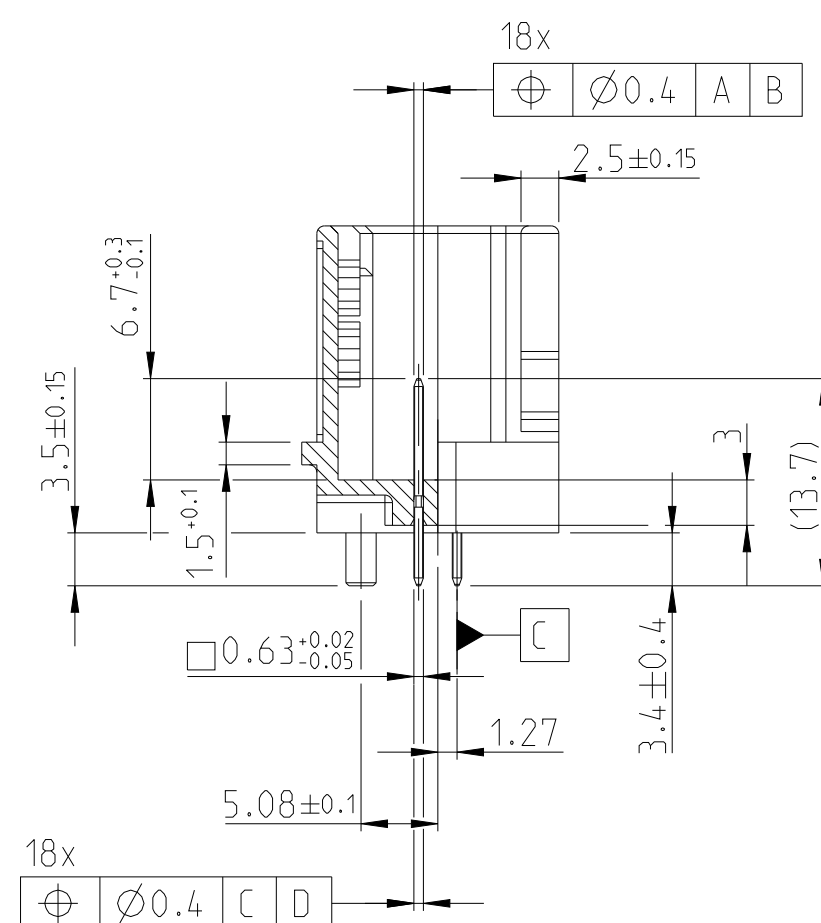
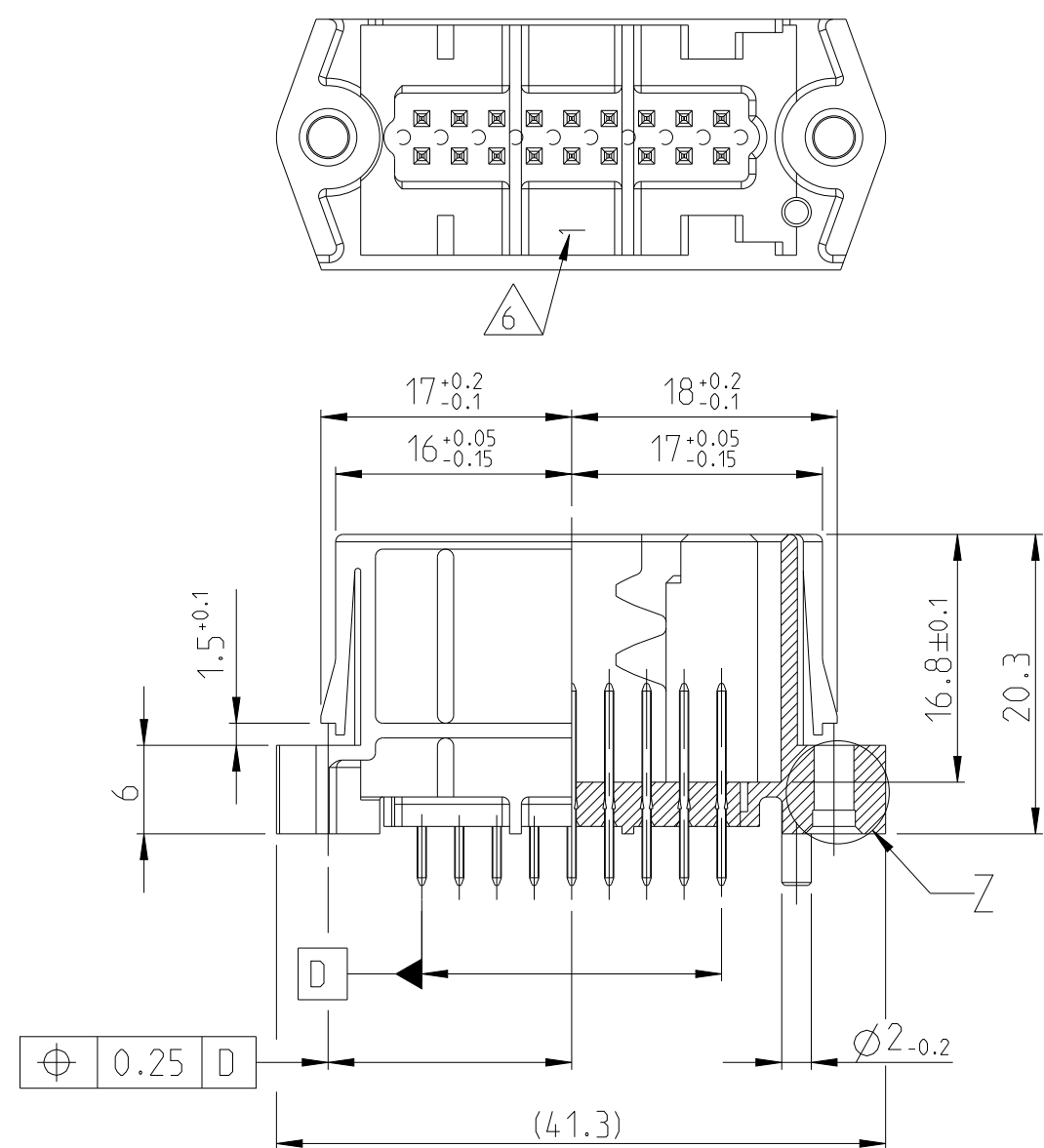
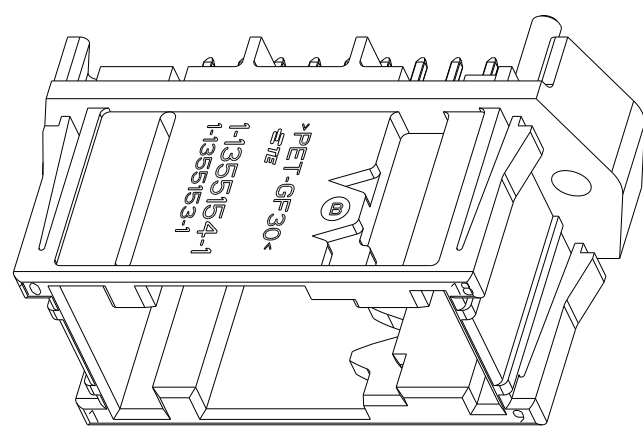


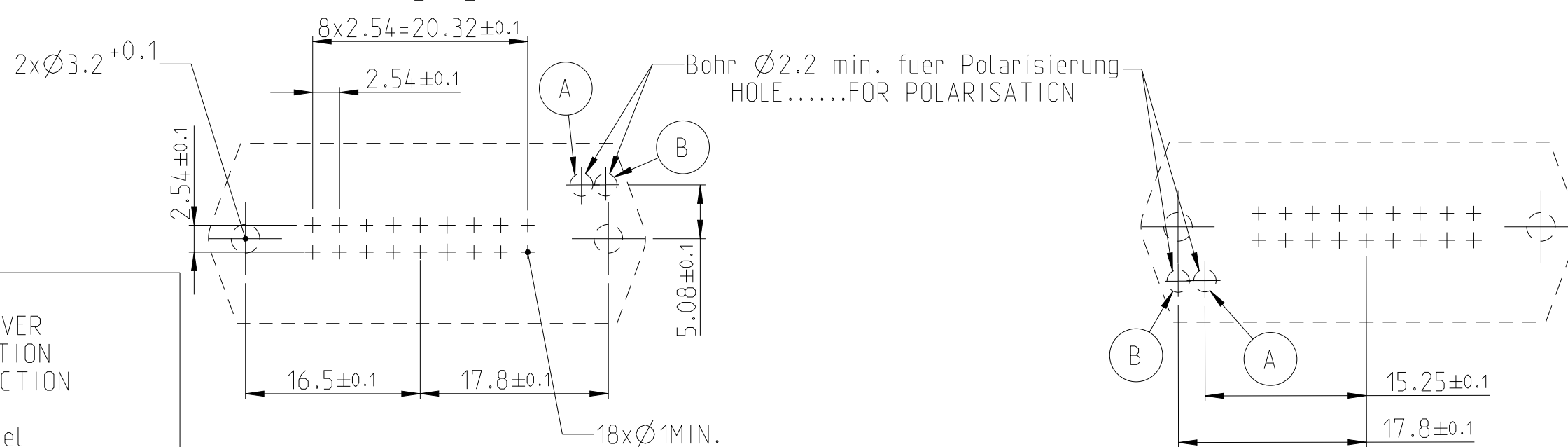


CABLE DIRECTION LEFT
Kabelabgang Links

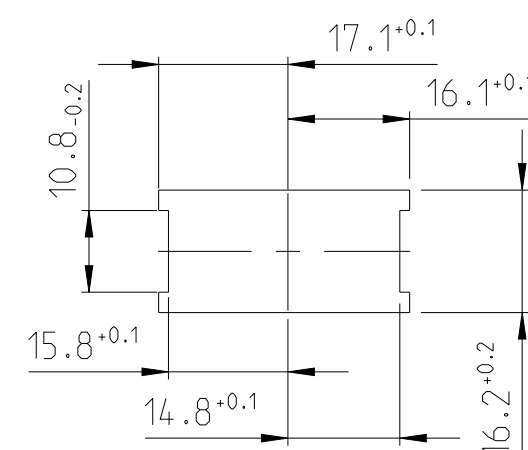
PCB LAYOUT

LeiterplattenLayout

CABLE DIRECTION RIGHT
Kabelabgang rechts

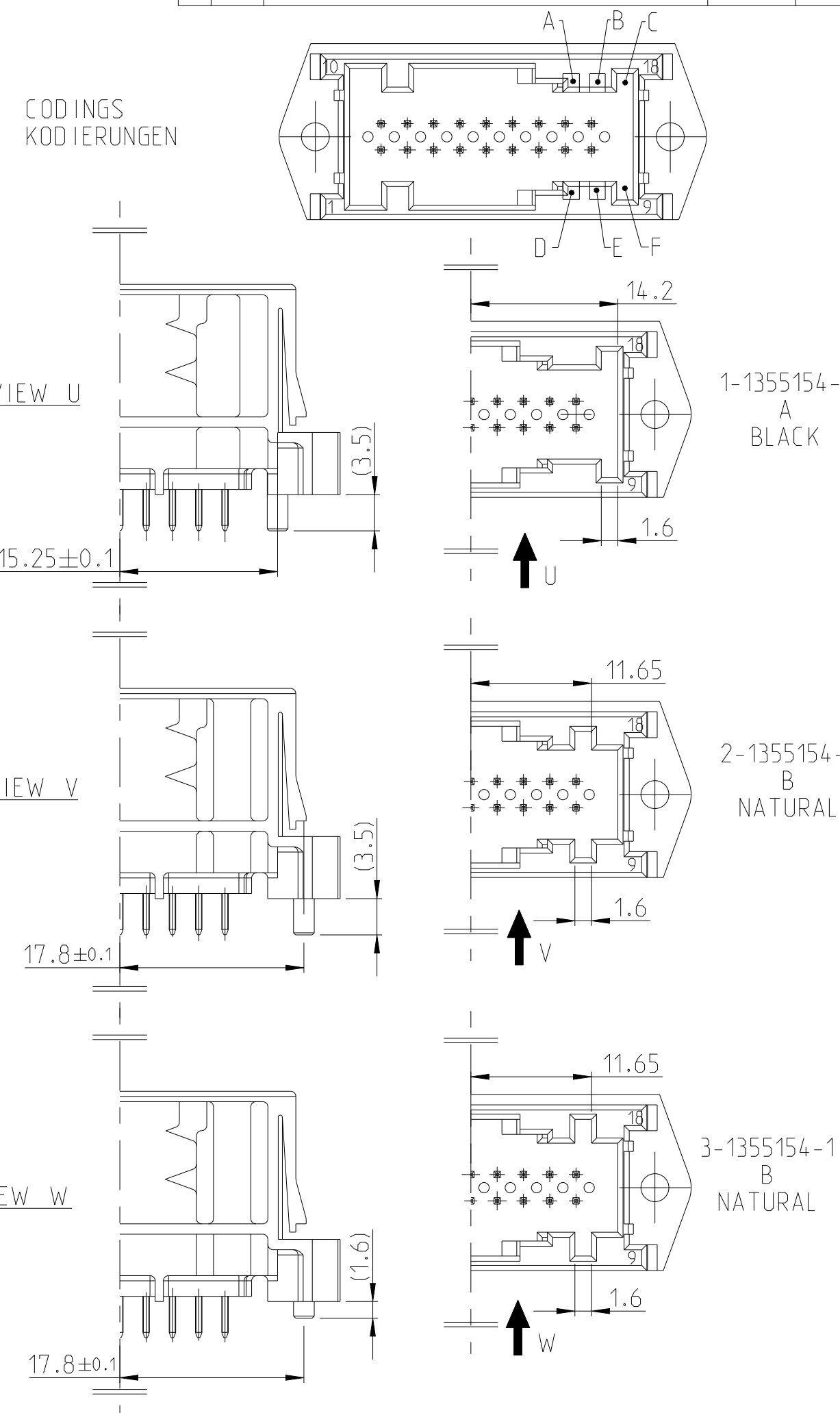


FRONT PLATE CUT-OUT
Frontplattenausschnitt
1:1



OBSOLETE

PROJECT NO.:		REVISIONS					
A98-52183		F	LTR	DESCRIPTION	DATE	DWN	APVD
		B3		PART NUMBER REARRANGED FOR MFG ON MODEL	06AUG2015	VGP	BF
		B4		PART NUMBER REARRANGED FOR MFG ON MODEL	20AUG2015	VGP	BF
		B5		ECR-23-196229	29DEC2023	MG	MBK



NOTES:
Bemerkungen:

- 1 MATERIALMARKING
Materialkennzeichnung
- 2 WARPAGE -0.3MM PERMITTED
Verzug -0.3mm zulaessig
- 3 PACKAGING: IN TRAY NO. 740966-1
Verpackung: in Produkttraeger 740966-1
- 4 PRODUCTSPECIFICATION: 108-18030
Produktspezifikation: 108-18030
- 5 PRESS OUT FORCE > 25N
SOLDERABILITY ACC. TO DIN IEC 68 Part 2-20
Ausdrueckkraft > 25N
Loefbarkeit gemaess DIN IEC 68 Teil 2-20.Alterung 3
- 6 CAVITY MARKING
Nestkennzeichnung
- 7 TE PN FOR HOUSING
TE Teile Nr. fuer Gehaeuse
- 8 TE PN FOR ASSY
TE Teile Nr. fuer Assembly
- 9 REVISION (HOUSING)
Aenderungsstand (Gehaeuse)
- 10 OTHER CODINGS UPON REQUEST
weitere Kodierungen auf Anfrage

TE Connectivity

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN O.Frank	24 JUN1999	 TE Connectivity
		CHK B.Fieberling	24 JUN1999	
		- NAME		
DIMENSIONS:		APPRO		MICRO QUADLOCK SYSTEM, PIN HEADER, 18POS Micro Quadlock System, Stiftwanne, 18pol.
mm	TOLERANCES UNLESS OTHERWISE SPECIFIED:	- PRODUCT SPEC		
	0 PLC ± 0.2	- APPLICATION SPEC		
	1 PLC ± 0.2	-		
	2 PLC ± 0.2	-		
	3 PLC $\pm$	-		
4 PLC $\pm$	ANGLES $\pm 2^\circ$	- WEIGHT		SIZE
MATERIAL	FINISH	ca. 6g		CAGE CODE
SEE TABLE siehe Tabelle	SEE TABLE siehe Tabelle	CUSTOMER DRAWING		DRAWING NO
				A1 00779
				C=1355154
				RESTRICTED TO
				SCALE 2:1
				SHEET 1 OF 1
				REV B5

1-1355154-1 AS SHOWN
WIE GEZEICHNET