

200G QSFP56 Active Optical Cable

FEATURES

- Four-channel full duplex active optical cable
- Up to 53.125 Gbps data rate per channel by PAM 4 modulation
- Low power consumption: < 4 W per cable end
- Single 3.3 V power supply
- Maximum link length of 70m on OM3 or 100m on OM4
- Management interface compliant with CMIS 4.0
- Hot pluggable QSFP56 form factor
- Commercial operating case temperature range: 0 to 70°C
- RoHS compliant



APPLICATION

• 200G Ethernet

PRODUCT SELECTION

Parameter	Length (m)		
2368651-1	1		
2368651-2	2		
2368651-3	3		
2368651-4	5		
2368651-5	10		
2368651-6	15		
2368651-7	20		
2368651-8	30		
Note: For availability of additional cable lengths, please contact TE.			



1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Unit
Power Supply Voltage	Vcc	-0.5	3.6	V
Storage Temperature	Tst	-40	85	°C
Case Operating Temperature	Тор	0	70	°C
Relative Humidity (non-condensing)	RH	0	85	%

2. RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Min	Тур	Max	Unit	Note
Power Supply Voltage	VCC	3.135	3.3	3.465	V	
Power Supply Current	ICC	-	-	1.28	А	per cable end
Power Dissipation	Р	-	-	4	W	per cable end
Bit Rate	BR	-	26.5625	-	Gbd	each channel

3. GENERAL PRODUCT CHARACTERISTICS

Parameter	Value	Notes		
Module Form Factor	QSFP56			
Number of Lanes	4 Tx and 4 Rx			
Maximum Aggregate Data Rate	212.5 Gbps			
Maximum Data Rate per Lane	26.5625 Gbd			
Bit Error Ratio, Pre-FEC	2.4x10 ⁻⁴	Tested with PRBS31		
Standard Cable Lengths	1, 2, 3, 5, 10, 15, 20, 30	Other lengths may be available upon request		
Electrical Interface and Pin-out	38-pin edge connector	Pin-out as defined by QSFP MSA SFF- 8679		
Standard Optical Cable Type	Multimode round fiber cable, OFNR and Low Smoke Zero Halogen (LSZH)	OFNP rated cable may be available upon request		
Maximum Power Consumption per Cable End	4 W			
Management Interface	Serial, I2C-based, 400kHz maximum frequency	As defined by CMIS 4.0		

4. ELECTRICAL CHARACTERISTICS

Low speed electrical specifications are compliant with SFF-8679 clause 5.

High speed electrical specifications are compliant with SFF-8679 clause 5 and IEEE802.3bs Annex 120E over operating case temperature 0 to 70° C and VCC 3.3 ± 5% Volts.

Min	Мах	Unit	Note
Module Electric	al Input		-
900	-	mV	1
		dB	
IEEE802.3bm Equation (83E–6)		dB	
-	10	%	
IEEE802.3bs 120E.3.4.1			2
-350	2850	mV	3
Module Electric	cal Output	•	•
-	17.5	mV	
-	900	mV	
0.265	-	UI	
70	-	mV	
0.2		UI	
30		mV	
-4.5	2.5	%	
IEEE802.3bm Equation (83E–2)		dB	
IEEE802.3bm Equation (83E–3)		dB	
	10	10%	
9.5		ps	
-350	2850	mV	3
	Module Electrica 900 IEEE80 Equation IEEE81 Equation IEEE81 Equation IEEE81 120E IEE83 120E Module Electrica 0.265 70 0.265 70 0.265 70 0.265 70 0.2 30 -4.5 IEEE80 Equation IEE81 9.5	Module Electric=Input 900 - IEEE8 .3bm Equatio $83E-5$) IEEE8 .3bm Equatio $83E-6$) IEEE8 .3bm Equatio 10 IEEE8 .3bs 120E .4.1 IEE8 .2850 120E .2850 Module Electric Output - 17.5 0.265 - 0.265 - 0.265 - 0.2 .2.5 30 .2.5 IEEE8 .3bm Equatio .32.5 IEEE8 .3bm Equatio 83E-2) IEEE8 .3bm Equatio .30 1EEE8 .3bm Equatio .3bm	Module Electric Input 900 - mV $ EEE8 >.3bm Equation (83E-5) dB EEE8 >.3bm Equation (83E-6) mV EEE8 >.3bm Equation (83E-6) mV 100 Electric mV -101 = 0.000 mV mV $

1. The pattern is PRBS31Q or scrambled idle

2. Meets specified BER

3. DC common-mode voltage generated by the host. Specification includes effects of ground offset voltage



5. PIN ASSIGNMENT

Pin assignment is compliant with SFF-8679.

6. MEMORY MAP

The memory map is compatible with CMIS 4.0, and customization can be supported.

7. MECHANICAL SPECIFICATIONS

Unit: mm





